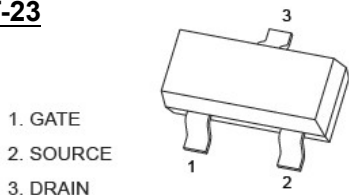
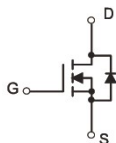


V(BR)DSS	RDS(ON)MAX	ID
60V	5Ω@10V 7Ω@5V	115mA

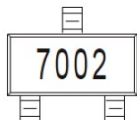
SOT-23



Equivalent Circuit



MARKING



特征 Features

- High density cell design for low $R_{DS(ON)}$.
- Voltage controlled small signal switch.
- Rugged and reliable.
- High saturation current capability.
- Load Switch for Portable Devices.
- DC/DC Converter.

机械数据 Mechanical Data

- 封装: SOT-23 封装 SOT-23 Small Outline Plastic Package.
- 环氧树脂 UL 易燃等级 Epoxy UL: 94V-0.
- 安装位置: 任意 Mounting Position: Any.

极限值和温度特性(TA = 25°C 除非另有规定)

Maximum Ratings & Thermal Characteristics (Ratings at 25°C ambient temperature unless otherwise specified.)

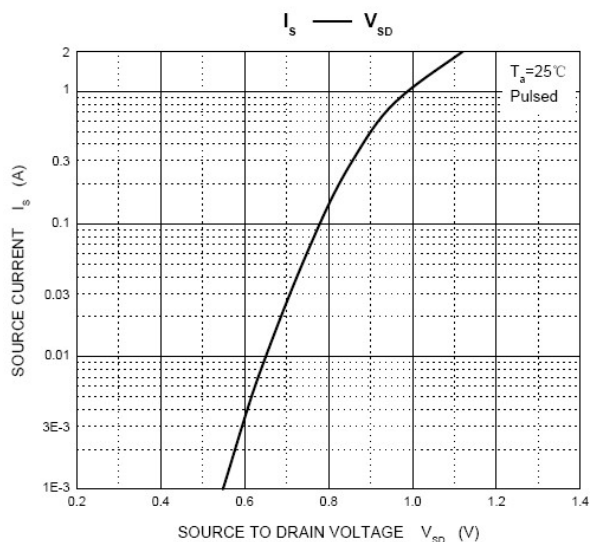
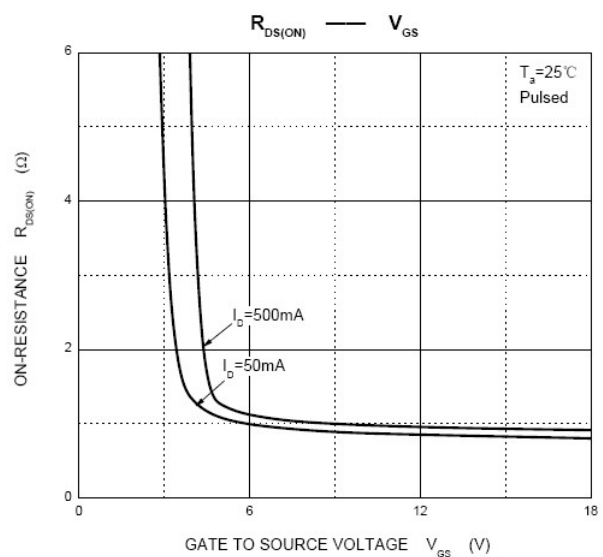
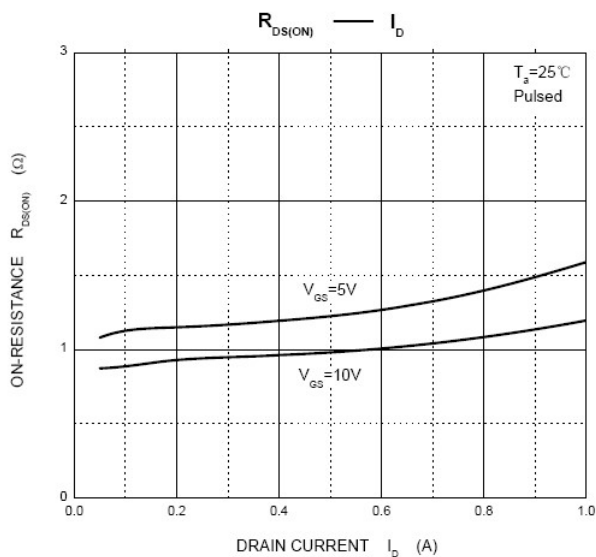
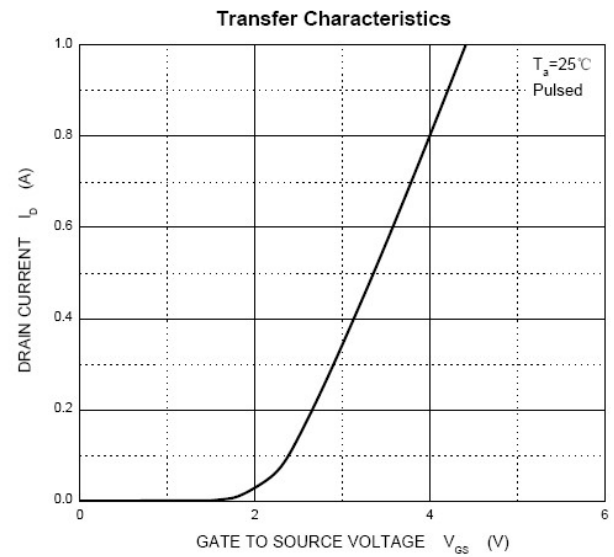
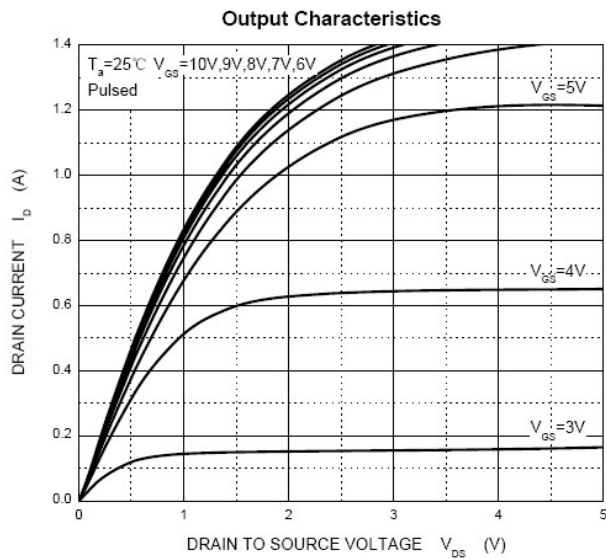
参数 Parameters	符号 Symbol	数值 Value	单位 Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	115	mA
Power Dissipation	P_D	225	mW
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-50-+150	°C
Thermal Resistance From Junction to Ambient	$R_{\theta JA}$	556	°C/W

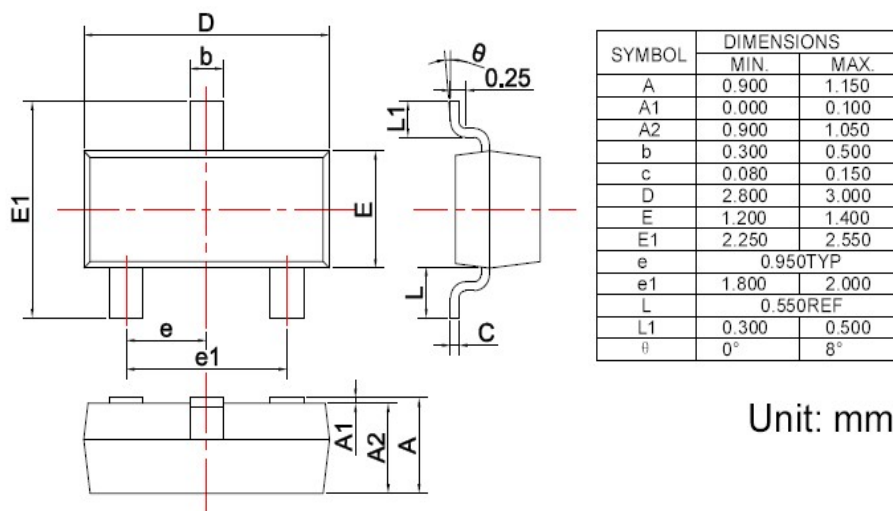
电特性 (TA = 25°C 除非另有规定)

Electrical Characteristics (Ratings at 25°C ambient temperature unless otherwise specified.)

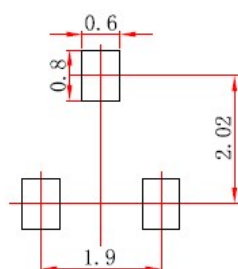
参数 Parameter	符号 Symbols	测试条件 Test Condition	界限 Limits		单位 Unit
			Min	Max	
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V, ID=250uA	60		V
Gate-Threshold voltage	Vth(GS)	VDS=VGS, ID=250uA	1	2.5	V
Gate-body Leakage	IGSS	VDS=0V, VGS=±20V		±10	uA
Zero Gate Voltage Drain current	IDSS	VDS=60V, VGS=0V		80	nA
On-state Drain current	ID(ON)	VGS=10V, VDS=7V	500		mA
Drain-Source On-Resistance	RDS(ON)	VGS=10V, ID=500mA		5	Ω
		VGS=5V, IC=50mA		7	
Forward Trans conductance	gfs	VDS=10V, ID=200mA	80		ms
Drain-source on-voltage	VDS(ON)	VGS=10V, ID=500mA		3.75	V
		VGS=5V, ID=50mA		0.375	V
Diode Forward voltage	VSD	IS=115mA, VGS=0V	0.55	1.20	V
Input capacitance*	Ciss	VDS=25V, VGS=0V,f=1MHz		50	pF
Output capacitance*	Coss			25	
Reverse Transfer capacitance*	Crss			5	
SWITCHING TIME					
Turn-on Time*	td(on)	VDD=25V, RL=50Ω, ID=500mA, VGEN=10V, RG=25Ω		20	ns
Turn-off Time*	td(off)			40	

* These parameters have on way to verify.

Typical characteristics


SOT-23 PACKAGE OUTLINE Plastic surface mounted package

焊盘设计参考Precautions: PCB Design

Recommended land dimensions for SOT-23 diode. Electrode patterns for PCBs


Note:

1. Controlling dimension: In millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

NOTICE

The information presented in this document is for reference only. Involving product optimization and productivity improvement, ChipNobo reserves the right to adjust product indicators and upgrade some technical parameters. ChipNobo is entitled to be exempted from liability for any delay or non-delivery of the information disclosure process that occurs.

本文件中提供的信息仅供参考。涉及产品优化和生产效率改善，ChipNobo 有权调整产品指标和部分技术参数的升级，所出现信息披露过程存在延后或者不能送达的情形，ChipNobo 有获免责权。

The product listed herein is designed to be used with residential and commercial equipment, and do not support sensitive items and specialized equipment in areas where sanctions do exist. ChipNobo Co., Ltd or anyone on its behalf, assumes no responsibility or liability for any damages resulting from improper use.

此处列出的产品旨在民用和商业设备上使用，不支持确有制裁地区的敏感项目和特殊设备，ChipNobo 有限公司或其代表，对因不当使用而造成的任何损害不承担任何责任。

For additional information, please visit our website <http://www.chipnobo.com>, or consult your nearest Chipnobo sales office for further assistance.

欲了解更多信息，请访问我们的网站 <http://www.chipnobo.com>，或咨询离您最近的 Chipnobo 销售办事处以获得进一步帮助。