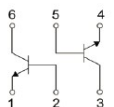
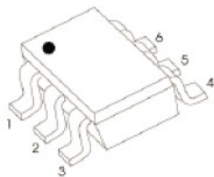


SOT-363

Marking: 4Ft
特征 Features

- Two transistors in one package
- Reduces number of components and board space
- No mutual interference between the transistors

机械数据 Mechanical Data

- SOT-363 Small Outline Plastic Package
- Epoxy UL: 94V-0
- Mounting Position: Any

极限值和温度特性(TA = 25°C 除非另有规定)

Maximum Ratings & Thermal Characteristics (Ratings at 25°C ambient temperature unless otherwise specified.)

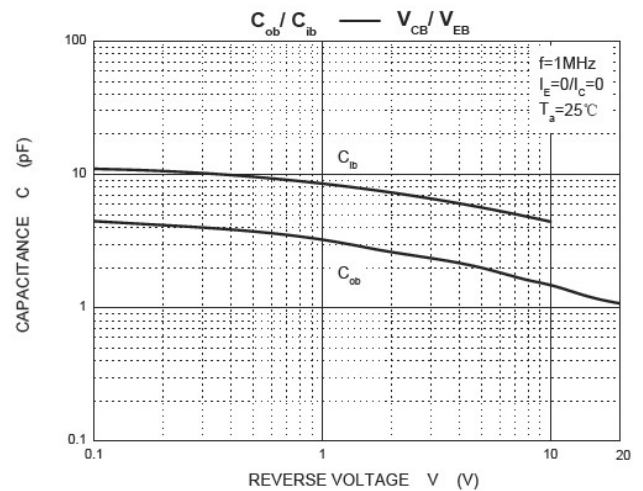
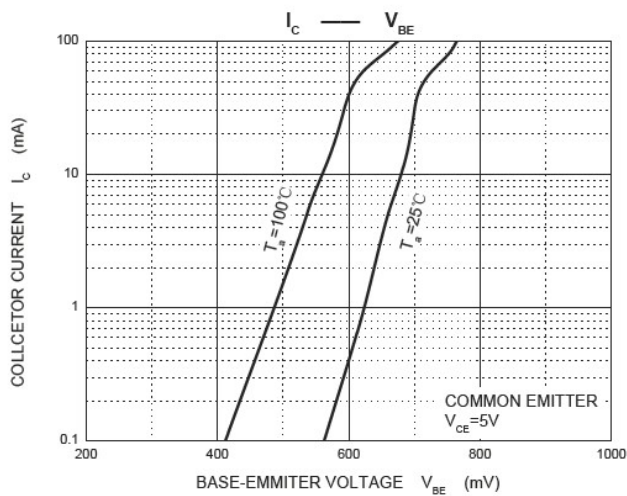
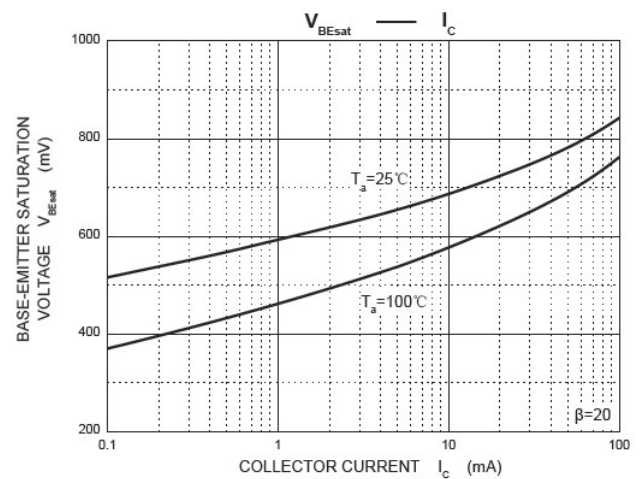
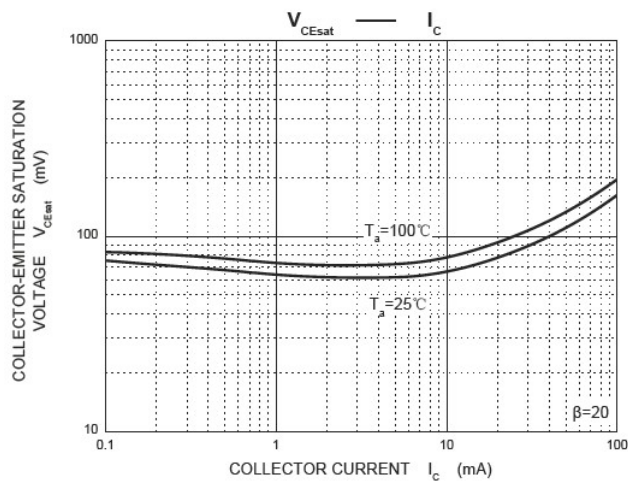
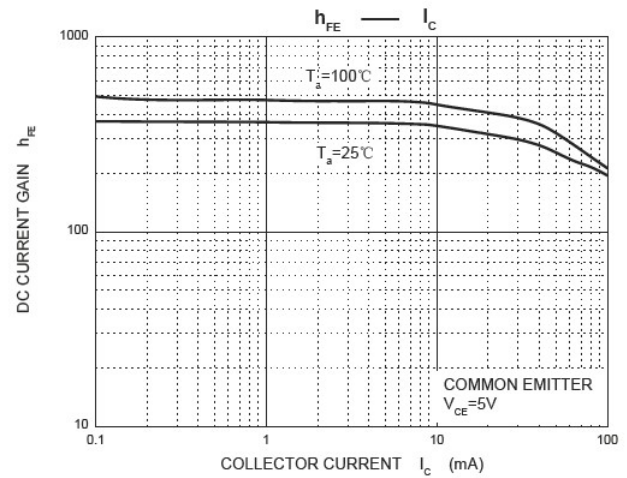
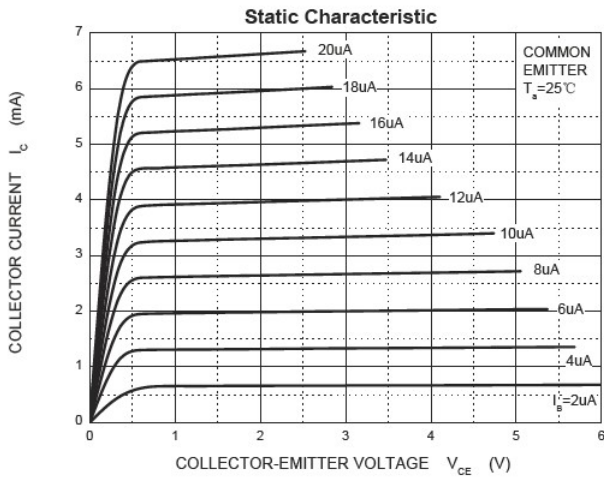
参数 Parameters	符号 Symbol	数值 Value	单位 Unit
Collector-Base Voltage	V _{CBO}	80	V
Collector-Emitter Voltage	V _{CEO}	65	V
Emitter -Base Voltage	V _{EBO}	6	V
Collector Current-Continuous	I _C	100	mA
Collector Power Dissipation	P _C	200	mW
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55-+150	°C

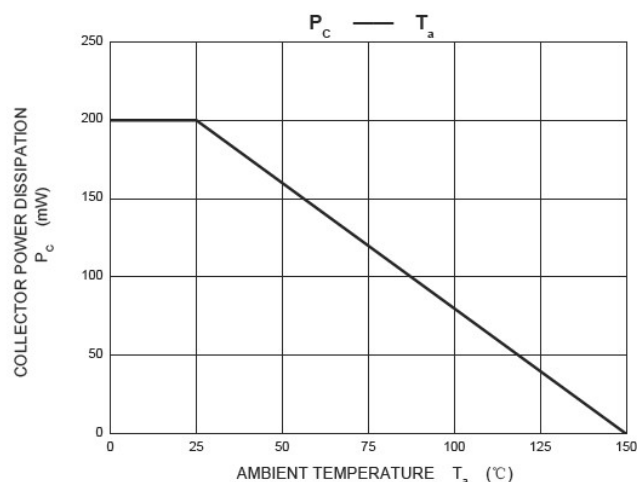
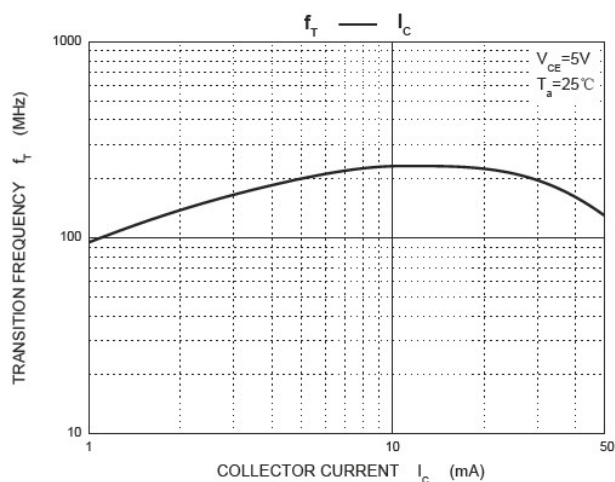
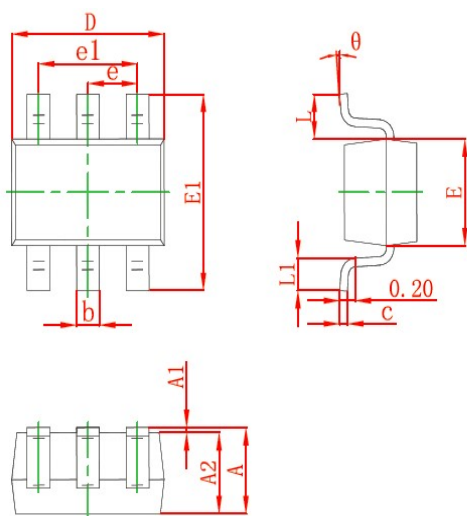
电特性 (TA = 25°C 除非另有规定)

Electrical Characteristics (Ratings at 25°C ambient temperature unless otherwise specified).

参数 Parameter	符号 Symbols	测试条件 Test Condition	界限 Limits			单位 Unit
			Min	Typ	Max	
Collector-base breakdown voltage	V _{(BR)CBO}	I _C =10uA, I _E =0	80			V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =1mA, I _B =0	65			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E =10uA, I _C =0	6			V
Collector cut-off current	I _{CBO}	V _{CB} =30V, I _E =0			15	nA
Emitter cut-off current	I _{EBO}	V _{EB} =5V, I _C =0			5	uA
DC current gain	h _{FE}	V _{CE} =5V, I _C =2mA	200		450	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =10mA, I _B =0.5mA			0.10	V
		I _C =100mA, I _B =5mA			0.30	V
Base -emitter saturation voltage	V _{BE(sat)}	I _C =10mA, I _B =0.5mA	0.58		0.70	V
		V _{CE} =5V, I _C =10mA		0.77		V
Transition frequency	f _T	V _{CE} =5V, I _C =20mA, f=100MHz	100			MHz
Collector output capacitance	C _{ob}	V _{CB} =10V, I _E =0, f=1MHz			1.5	pF

*Pulse test: pulse width≤300us,duty cycle≤2.0%

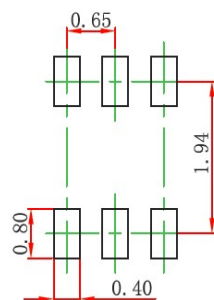
Typical characteristics



SOT-363 PACKAGE OUTLINE Plastic surface mounted package


Symbol	Dimensions In Millimeters	
	Min	Max
A	0.900	1.100
A1	0.000	0.100
A2	0.900	1.000
b	0.150	0.350
c	0.100	0.150
D	2.000	2.200
E	1.150	1.350
E1	2.150	2.400
e	0.650 TYP	
e1	1.200	1.400
L	0.525 REF	
L1	0.260	0.460
θ	0°	8°

焊盘设计参考 Precautions: PCB Design

Recommended land dimensions for SOT-363 diode. Electrode patterns for PCBs


Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05mm$.
3. The pad layout is for reference purposes only.

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