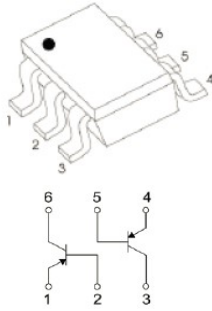


SOT-363



特征 Features

- Two transistors in one package
- Reduces number of components and board space
- No mutual interference between the transistors

机械数据 Mechanical Data

- SOT-363 Small Outline Plastic Package
- Epoxy UL: 94V-0
- Mounting Position: Any

极限值和温度特性(TA = 25°C 除非另有规定)

Maximum Ratings & Thermal Characteristics (Ratings at 25°C ambient temperature unless otherwise specified.)

参数 Parameters	符号 Symbol	数值 Value	单位 Unit
Collector-Base Voltage	V _{CBO}	-50	V
Collector-Emitter Voltage	V _{CEO}	-45	V
Emitter -Base Voltage	V _{EBO}	-5	V
Collector Current-Continuous	I _C	-200	mA
Collector Power Dissipation	P _C	300	mW
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55-+150	°C
Thermal resistance From junction to ambient	R _{θJA}	417	°C/W

电特性 (TA = 25°C 除非另有规定)

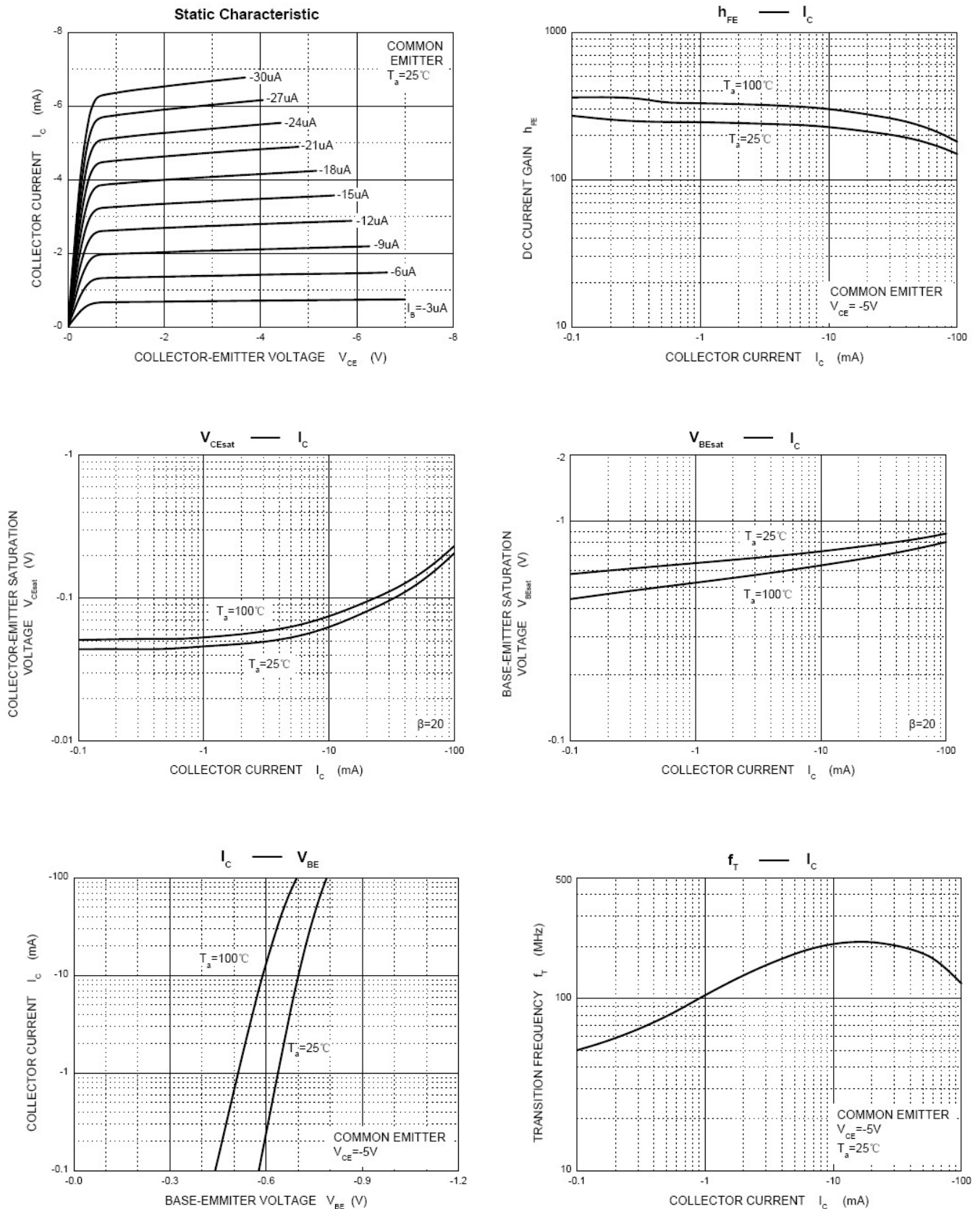
Electrical Characteristics (Ratings at 25°C ambient temperature unless otherwise specified.)

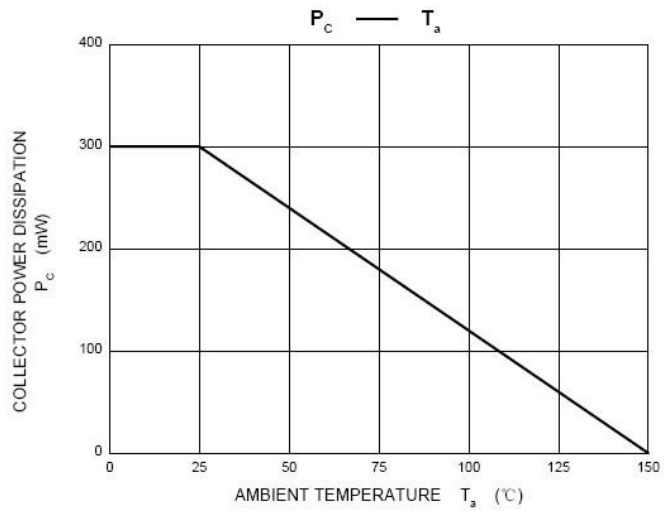
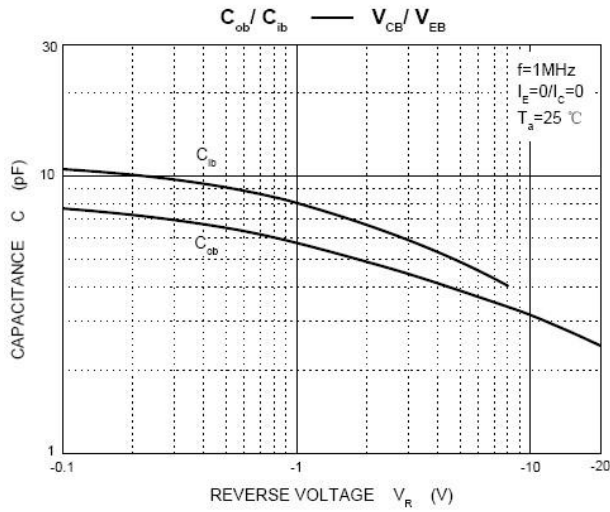
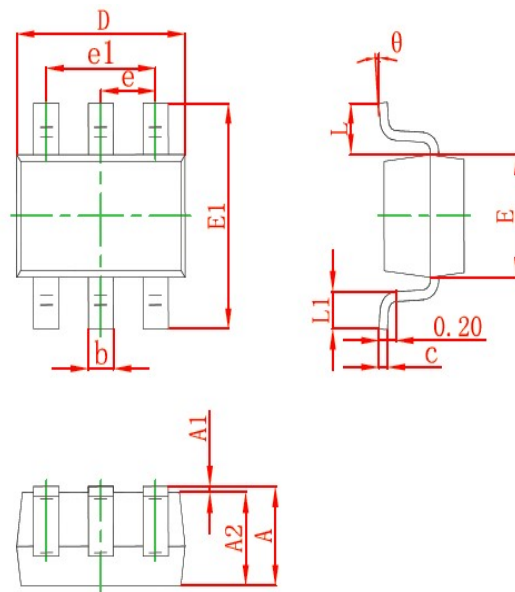
参数 Parameter	符号 Symbols	测试条件 Test Condition	界限 Limits			单位 Unit
			Min		Max	
Collector-base breakdown voltage	V(BR)CBO	IC=-10uA, IE=0	-50			V
Collector-emitter breakdown voltage	V(BR)CEO	IC=-10mA, IB=0	-45			V
Emitter-base breakdown voltage	V(BR)EBO	IE=-10uA, IC=0	-5			V
Collector cut-off current	ICBO	VCE=-30V, IE=0			-15	nA
Base cut-off current	IEBO	VEB=-5V, IC=0			-100	nA
DC current gain	hFE	VCE=-5V, IC=-2mA	125		630	
Collector-emitter saturation voltage	VCE(sat)	IC=-10mA, IB=-0.5mA			-0.3	V
		IC=-100mA, IB=-5mA			-0.65	V
Base -emitter voltage	VBE	VCE=-5V, IB=-2mA	-0.6		-0.75	V
		VCE=-5V, IB=-10mA			-0.82	V
Transition frequency	f _T	VCE=-5V, IC=-10mA, f=100MHz		200		MHz
Collector output capacitance	C _{ob}	VCE=-10V, IE=0, f=1MHz		3.5		pF
Noise figure	NF	VCE=-5V, IC=-0.2mA, f=1KHz; Rs=2KΩ, BW=200Hz		2.5		dB

*Pulse test: pulse width≤350us,duty cycle≤2.0%

CLASSIFICATION OF hFE

RANK	BC857BDW1T1G-CN
RANGE	125-630
MARKING	3C

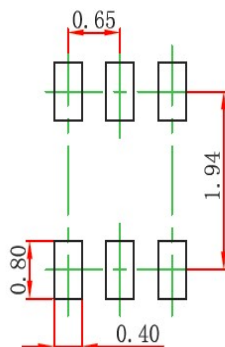
Typical characteristics



SOT-363 PACKAGE OUTLINE Plastic surface mounted package


Symbol	Dimensions In Millimeters	
	Min	Max
A	0.900	1.100
A1	0.000	0.100
A2	0.900	1.000
b	0.150	0.350
c	0.100	0.150
D	2.000	2.200
E	1.150	1.350
E1	2.150	2.400
e	0.650 TYP	
e1	1.200	1.400
L	0.525 REF	
L1	0.260	0.460
θ	0°	8°

焊盘设计参考Precautions: PCB Design

Recommended land dimensions for SOT-363 diode. Electrode patterns for PCBs


Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

NOTICE

The information presented in this document is for reference only. Involving product optimization and productivity improvement, ChipNobo reserves the right to adjust product indicators and upgrade some technical parameters. ChipNobo is entitled to be exempted from liability for any delay or non-delivery of the information disclosure process that occurs.

本文件中提供的信息仅供参考。涉及产品优化和生产效率改善，ChipNobo 有权调整产品指标和部分技术参数的升级，所出现信息披露过程存在延后或者不能送达的情形，ChipNobo 有获免责权。

The product listed herein is designed to be used with residential and commercial equipment, and do not support sensitive items and specialized equipment in areas where sanctions do exist. ChipNobo Co., Ltd or anyone on its behalf, assumes no responsibility or liability for any damages resulting from improper use.

此处列出的产品旨在民用和商业设备上使用，不支持确有制裁地区的敏感项目和特殊设备，ChipNobo 有限公司或其代表，对因不当使用而造成的任何损害不承担任何责任。

For additional information, please visit our website <http://www.chipnobo.com>, or consult your nearest Chipnobo sales office for further assistance.

欲了解更多信息，请访问我们的网站 <http://www.chipnobo.com>，或咨询离您最近的 Chipnobo 销售办事处以获得进一步帮助。