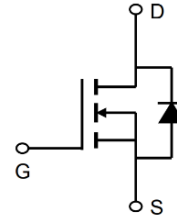


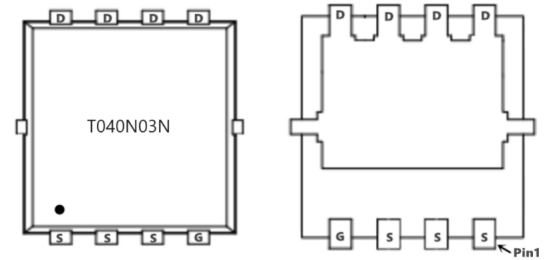
## General Features

| $V_{DS}$ | $I_D$ | $R_{DS(ON)}$ Type<br>@ $V_{GS} = 10V$ | $R_{DS(ON)}$ Type<br>@ $V_{GS} = 4.5V$ |
|----------|-------|---------------------------------------|----------------------------------------|
| 30V      | 80A   | 3.4m $\Omega$                         | 5.7m $\Omega$                          |



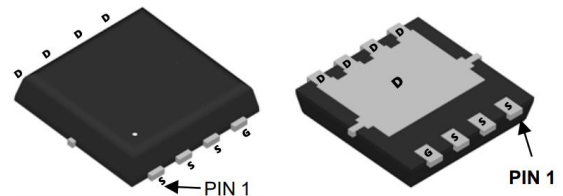
## Description

The AON7522E-CN uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



## Application

Battery protection  
Load switch  
Uninterruptible power supply



## Package Marking and Ordering Information

| Product ID  | Pack       | Marking  | Qty(PCS) |
|-------------|------------|----------|----------|
| AON7522E-CN | PDFN3*3-8L | T040N03N | 5000     |

## Absolute Maximum Ratings ( $T_C=25^{\circ}\text{C}$ unless otherwise noted)

| Symbol          | Parameter                                              | Value                       | Units                |
|-----------------|--------------------------------------------------------|-----------------------------|----------------------|
| $V_{DS}$        | Drain-to-Source Voltage                                | 30                          | V                    |
| $V_{GS}$        | Gate-to-Source Voltage                                 | $\pm 20$                    | V                    |
| $I_D$           | Continuous Drain Current                               | $T_C = 25^{\circ}\text{C}$  | A                    |
|                 |                                                        | $T_C = 100^{\circ}\text{C}$ |                      |
| $I_{DM}$        | Pulsed Drain Current <sup>(1)</sup>                    | 240                         | A                    |
| $E_{AS}$        | Single Pulsed Avalanche Energy <sup>(2)</sup>          | 121                         | mJ                   |
| $P_D$           | Power Dissipation                                      | $T_C = 25^{\circ}\text{C}$  | W                    |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient <sup>(3)</sup> | 41                          | $^{\circ}\text{C/W}$ |
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case                   | 3.2                         |                      |
| $T_J, T_{STG}$  | Junction & Storage Temperature Range                   | -55 to 150                  | $^{\circ}\text{C}$   |

**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

| Symbol                                             | Parameter                                                | Conditions                                                                                  | Min. | Typ. | Max. | Unit |
|----------------------------------------------------|----------------------------------------------------------|---------------------------------------------------------------------------------------------|------|------|------|------|
| Off Characteristics                                |                                                          |                                                                                             |      |      |      |      |
| V <sub>(BR)DSS</sub>                               | Drain-Source Breakdown Voltage                           | I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V                                                | 30   | -    | -    | V    |
| I <sub>DSS</sub>                                   | Zero Gate Voltage Drain Current                          | V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V                                                 | -    | -    | 1.0  | μA   |
| I <sub>GSS</sub>                                   | Gate-Body Leakage Current                                | V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V                                                | -    | -    | ±100 | nA   |
| On Characteristics                                 |                                                          |                                                                                             |      |      |      |      |
| V <sub>GS(th)</sub>                                | Gate Threshold Voltage                                   | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA                                  | 1.2  | 1.7  | 2.5  | V    |
| R <sub>DS(ON)</sub>                                | Static Drain-Source ON-Resistance <sup>(4)</sup>         | V <sub>GS</sub> = 10V, I <sub>D</sub> = 30A                                                 | -    | 3.4  | 4.0  | mΩ   |
|                                                    |                                                          | V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 20A                                                | -    | 5.7  | 7.4  | mΩ   |
| Dynamic Characteristics                            |                                                          |                                                                                             |      |      |      |      |
| C <sub>iss</sub>                                   | Input Capacitance                                        | V <sub>GS</sub> = 0V, V <sub>DS</sub> = 15V, f = 1MHz                                       | -    | 3089 | -    | pF   |
| C <sub>oss</sub>                                   | Output Capacitance                                       |                                                                                             | -    | 372  | -    | pF   |
| C <sub>rss</sub>                                   | Reverse Transfer Capacitance                             |                                                                                             | -    | 302  | -    | pF   |
| Q <sub>g</sub>                                     | Total Gate Charge                                        | V <sub>GS</sub> = 0 to 10V<br>V <sub>DS</sub> = 15V, I <sub>D</sub> = 30A                   | -    | 58   | -    | nC   |
| Q <sub>gs</sub>                                    | Gate Source Charge                                       |                                                                                             | -    | 12   | -    | nC   |
| Q <sub>gd</sub>                                    | Gate Drain("Miller") Charge                              |                                                                                             | -    | 13   | -    | nC   |
|                                                    |                                                          |                                                                                             |      |      |      |      |
| Switching Characteristics                          |                                                          |                                                                                             |      |      |      |      |
| t <sub>d(on)</sub>                                 | Turn-On DelayTime                                        | V <sub>GS</sub> = 10V, V <sub>DD</sub> = 15V<br>I <sub>D</sub> = 30A, R <sub>GEN</sub> = 3Ω | -    | 11   | -    | ns   |
| t <sub>r</sub>                                     | Turn-On Rise Time                                        |                                                                                             | -    | 29   | -    | ns   |
| t <sub>d(off)</sub>                                | Turn-Off DelayTime                                       |                                                                                             | -    | 47   | -    | ns   |
| t <sub>f</sub>                                     | Turn-Off Fall Time                                       |                                                                                             | -    | 18   | -    | ns   |
| Drain-Source Diode Characteristics and Max Ratings |                                                          |                                                                                             |      |      |      |      |
| I <sub>S</sub>                                     | Maximum Continuous Drain to Source Diode Forward Current |                                                                                             | -    | -    | 80   | A    |
| I <sub>SM</sub>                                    | Maximum Pulsed Drain to Source Diode Forward Current     |                                                                                             | -    | -    | 240  | A    |
| V <sub>SD</sub>                                    | Drain to Source Diode Forward Voltage                    | V <sub>GS</sub> = 0V, I <sub>S</sub> = 30A                                                  | -    | -    | 1.2  | V    |
| t <sub>rr</sub>                                    | Body Diode Reverse Recovery Time                         | I <sub>F</sub> = 30A, di/dt = 100A/us                                                       | -    | 16   | -    | ns   |
| Q <sub>rr</sub>                                    | Body Diode Reverse Recovery Charge                       |                                                                                             | -    | 7    | -    | nC   |

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2. E<sub>AS</sub> condition: Starting T<sub>J</sub>=25°C, V<sub>DD</sub>=15V, V<sub>G</sub>=10V, R<sub>G</sub>=25ohm, L=0.5mH, I<sub>AS</sub>=22A
  3. R<sub>θJA</sub> is measured with the device mounted on a 1inch<sup>2</sup> pad of 2oz copper FR4 PCB
  4. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

## Typical Performance Characteristics

Figure 1: Output Characteristics

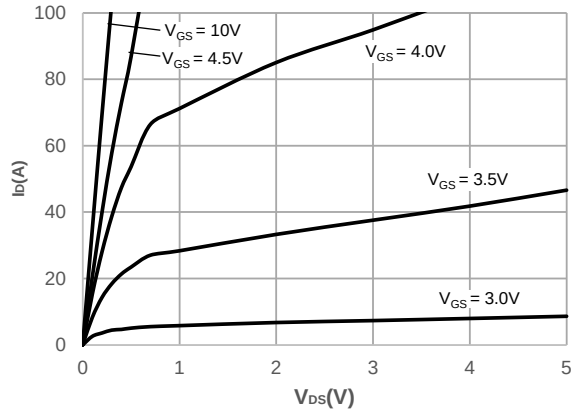


Figure 2: Typical Transfer Characteristics

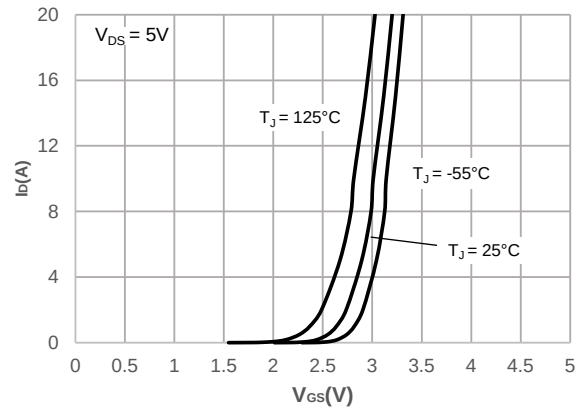


Figure 3: On-resistance vs. Drain Current

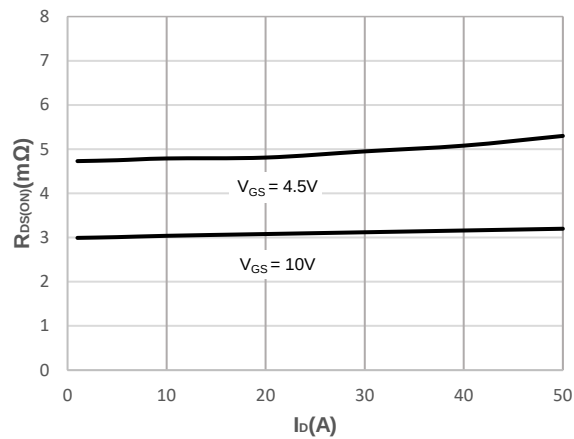


Figure 4: Body Diode Characteristics

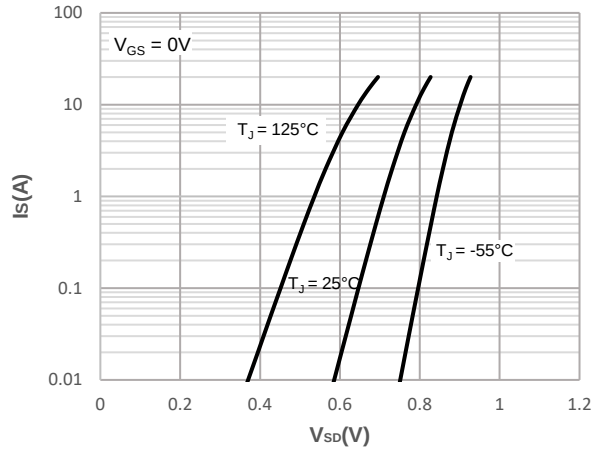


Figure 5: Gate Charge Characteristics

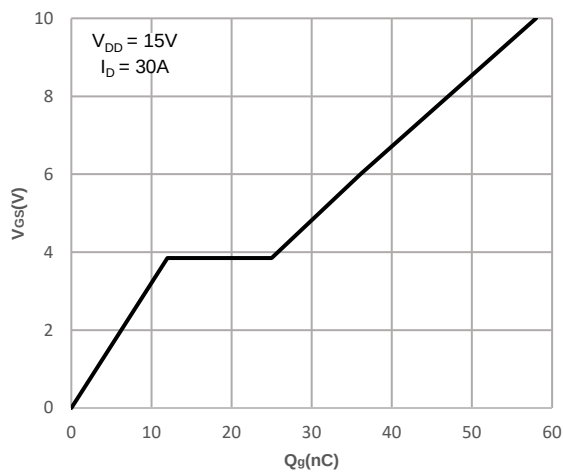


Figure 6: Capacitance Characteristics

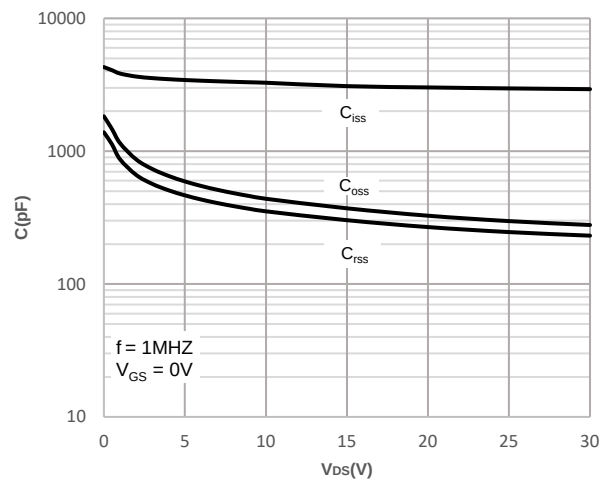


Figure 7: Normalized Breakdown voltage vs. Junction Temperature

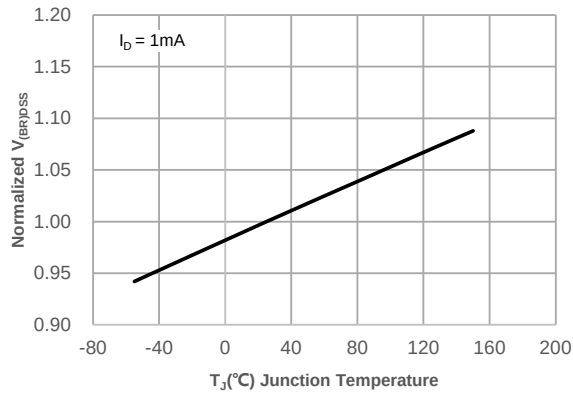


Figure 8: Normalized on Resistance vs. Junction Temperature

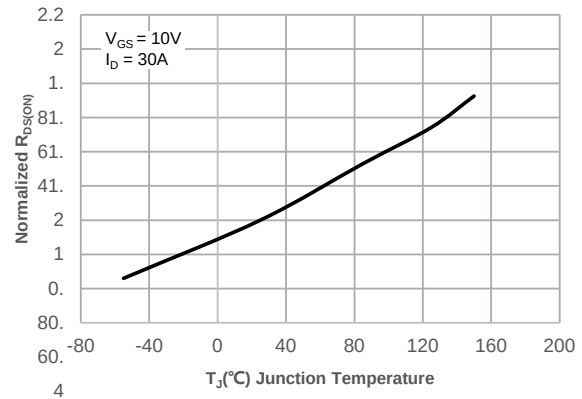


Figure 9: Maximum Safe Operating Area

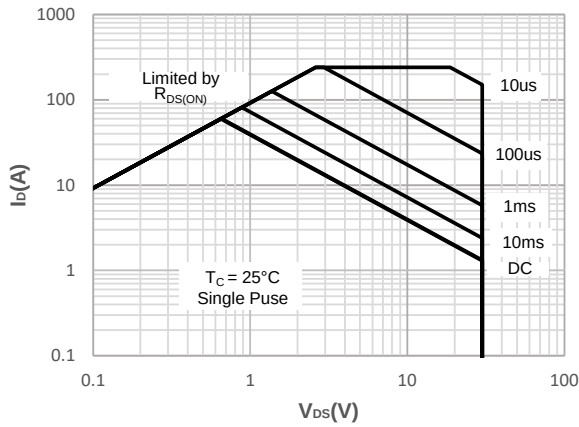


Figure 10: Maximum Continuous Drian Current vs. Case Temperature

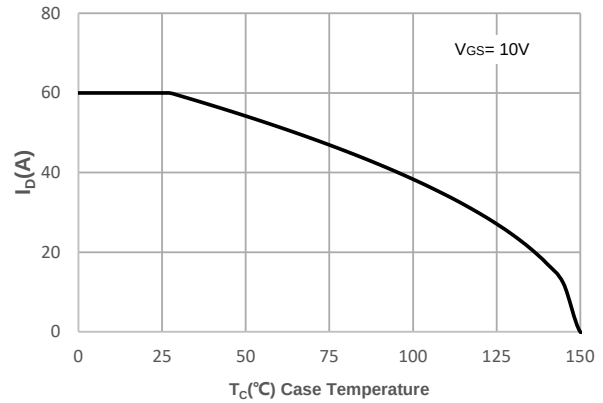


Figure 11: Normalized Maximum Transient Thermal Impedance

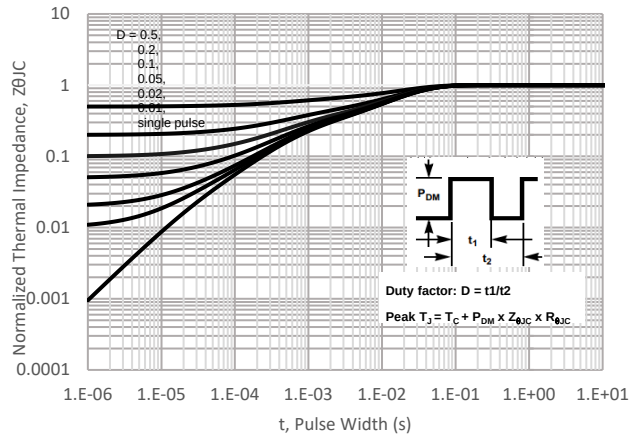
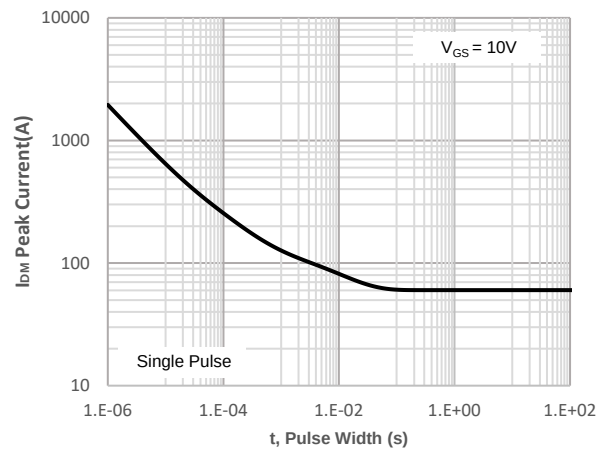
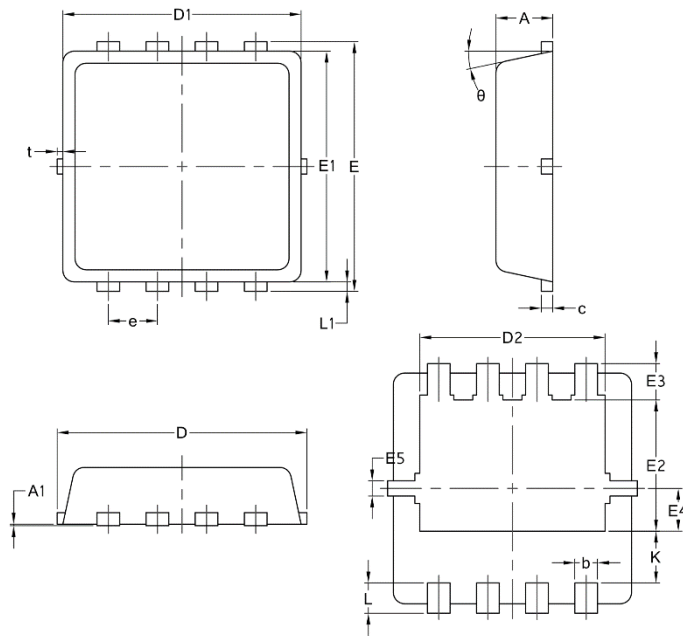


Figure 12: Peak Current Capacity



**Package Mechanical Data-PDFN3\*3-8L**


| Symbol | Common |       |      |
|--------|--------|-------|------|
|        | mm     |       |      |
|        | Mim    | Nom   | Max  |
| A      | 0.70   | 0.75  | 0.85 |
| A1     | /      | /     | 0.05 |
| b      | 0.20   | 0.30  | 0.40 |
| c      | 0.10   | 0.152 | 0.25 |
| D      | 3.15   | 3.30  | 3.45 |
| D1     | 3.00   | 3.15  | 3.25 |
| D2     | 2.29   | 2.45  | 2.65 |
| E      | 3.15   | 3.30  | 3.45 |
| E1     | 2.90   | 3.05  | 3.20 |
| E2     | 1.54   | 1.74  | 1.94 |
| E3     | 0.28   | 0.48  | 0.65 |
| E4     | 0.37   | 0.57  | 0.77 |
| E5     | 0.10   | 0.20  | 0.30 |
| e      | 0.60   | 0.65  | 0.70 |
| K      | 0.59   | 0.69  | 0.89 |
| L      | 0.30   | 0.40  | 0.50 |
| L1     | 0.06   | 0.125 | 0.20 |
| t      | 0      | 0.075 | 0.13 |
| Φ      | 10     | 12    | 14   |

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