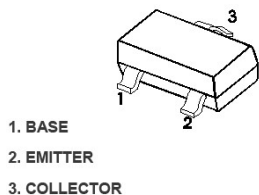


SOT-23



Marking: 1K

特征 Features

- Power Dissipation of 200mW
- Ideally suited for automatic insertion
- For switching and AF amplifier applications
- AEC-Q 101 qualified

机械数据 Mechanical Data

- Small Outline Plastic Package
- Epoxy UL: 94V-0
- Mounting Position: Any

极限值和温度特性(TA = 25°C 除非另有规定)

Maximum Ratings & Thermal Characteristics (Ratings at 25°C ambient temperature unless otherwise specified.)

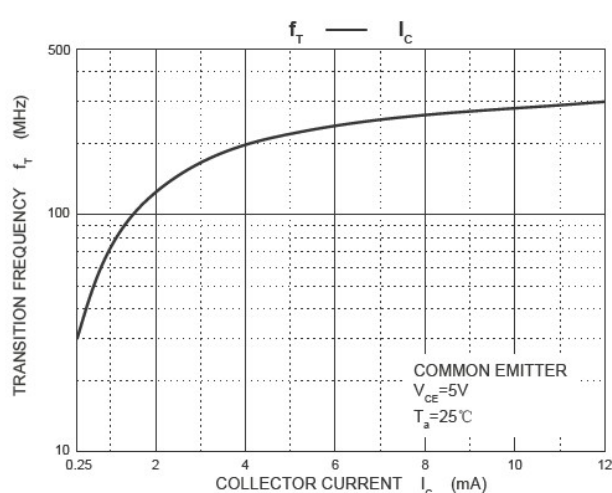
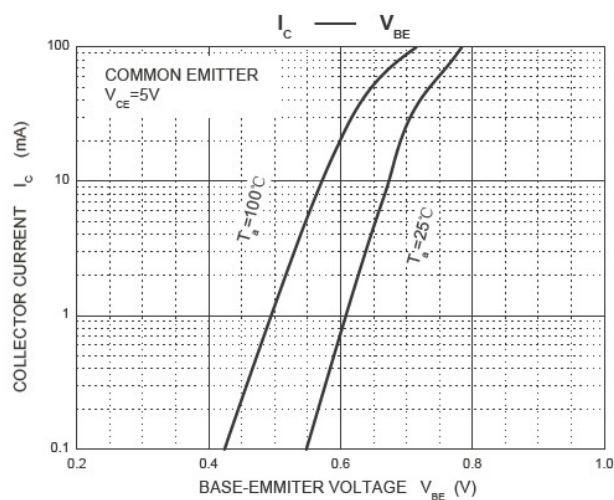
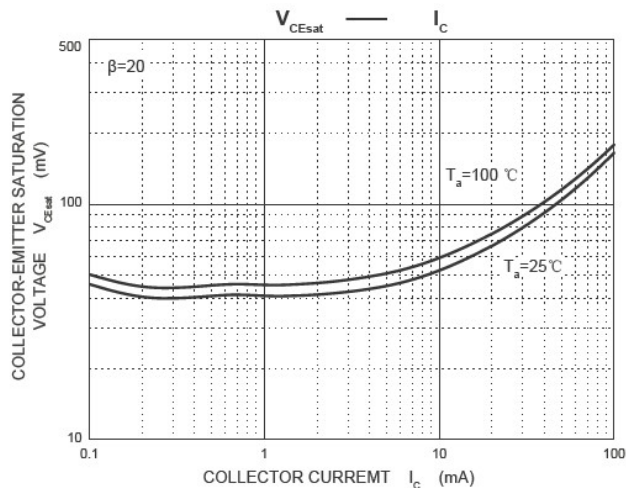
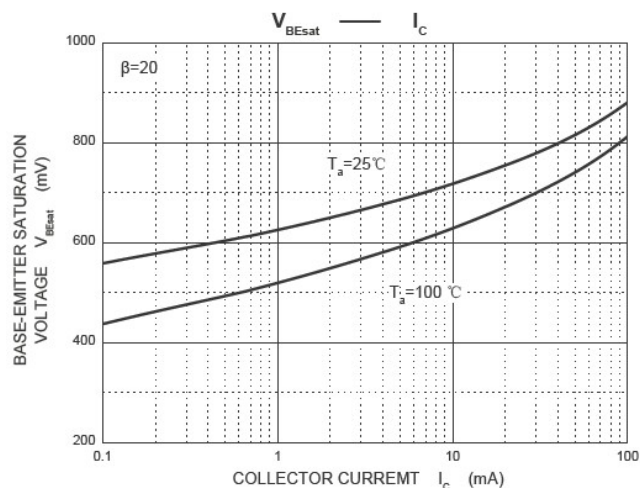
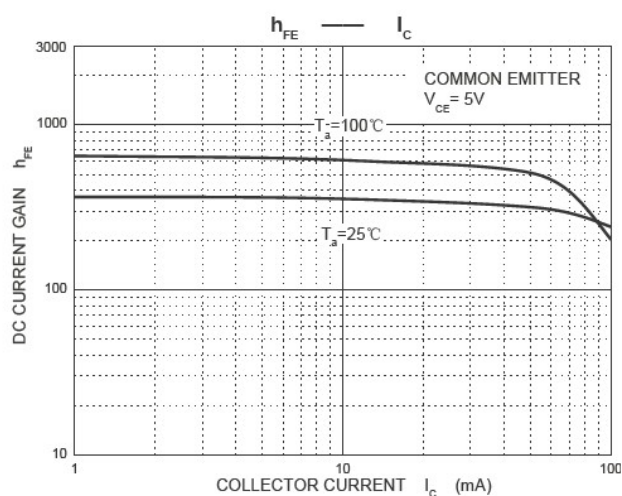
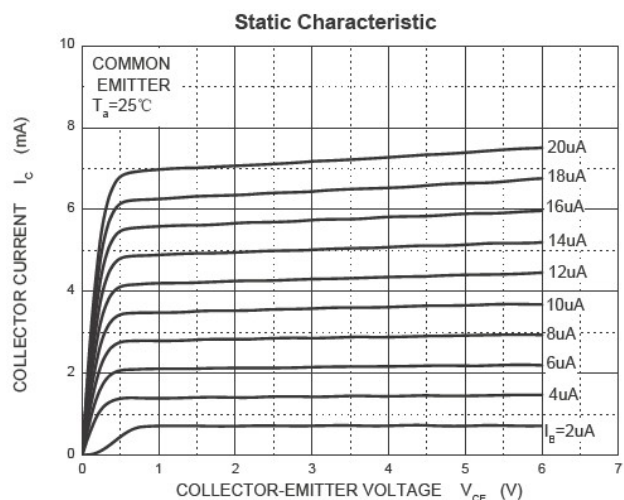
参数 Parameters	符号 Symbol	数值 Value	单位 Unit
Collector-Base Voltage	V_{CBO}	30	V
Collector-Emitter Voltage	V_{CEO}	30	V
Emitter -Base Voltage	V_{EBO}	6	V
Collector Current-Continuous	I_C	100	mA
Collector Power Dissipation	P_C	200	mW
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-55-+150	°C
Thermal resistance From junction to ambient	$R_{\theta JA}$	625	°C/W

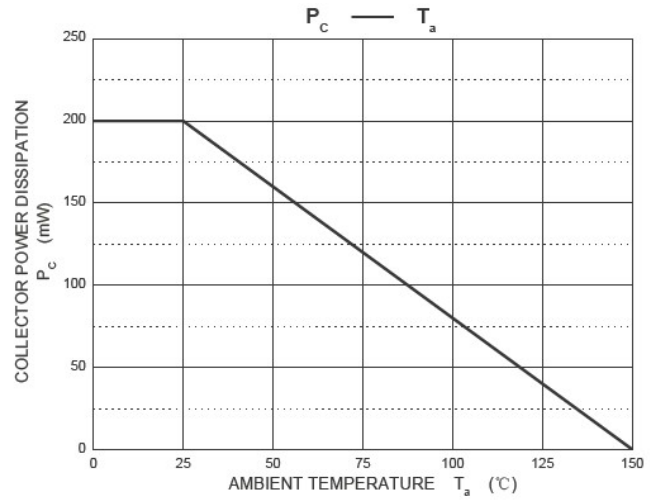
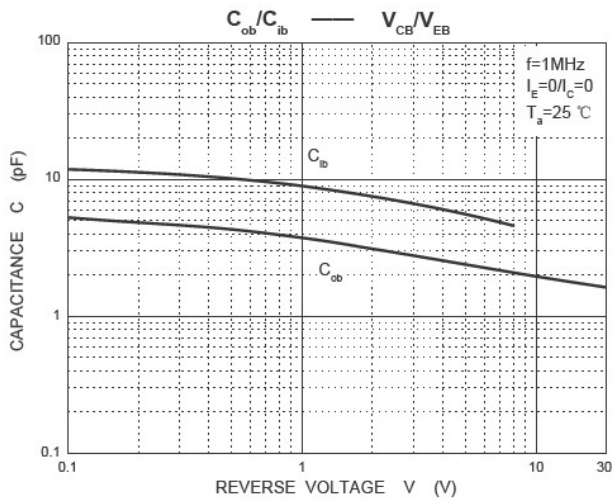
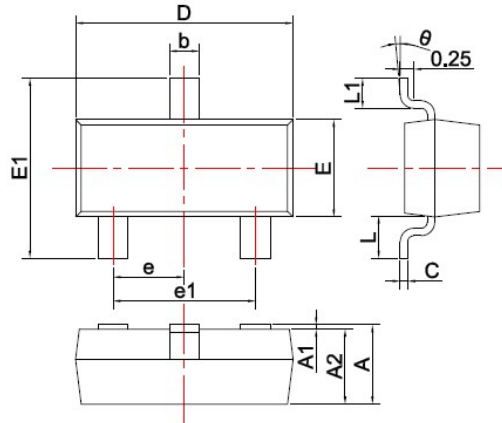
电特性 (TA = 25°C 除非另有规定)

Electrical Characteristics (Ratings at 25°C ambient temperature unless otherwise specified.)

参数 Parameter	符号 Symbols	测试条件 Test Condition	界限 Limits		单位 Unit
			Min	Max	
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C=10\mu A, I_E=0$	30		V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C=10mA, I_B=0$	30		V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E=10\mu A, I_C=0$	6		V
Collector cut-off current	I_{CBO}	$V_{CB}=30V, I_E=0$		100	nA
Collector cut-off current	I_{CEO}	$V_{CE}=30V, I_B=0$		100	nA
Emitter cut-off current	I_{EBO}	$V_{EB}=5V, I_C=0$		100	nA
DC current gain	h_{FE}	$V_{CE}=5V, I_C=2mA$	200	450	
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C=100mA, I_B=5mA$		0.50	V
Base -emitter saturation voltage	$V_{BE(sat)}$	$I_C=100mA, I_B=5mA$		1.10	V
Transition frequency	f_T	$V_{CE}=5V, I_C=10mA, f=100MHz$	100		MHz
Collector output capacitance	C_{ob}	$V_{CB}=10V, f=1MHz$		4.5	pF

Typical characteristics



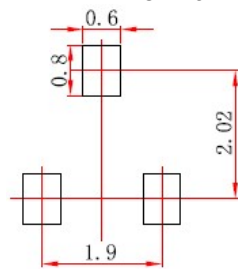

SOT-23 PACKAGE OUTLINE Plastic surface mounted package


SYMBOL	DIMENSIONS	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

Unit: mm

焊盘设计参考Precautions: PCB Design

Recommended land dimensions for SOT-23 diode. Electrode patterns for PCBs


Note:

1. Controlling dimension; in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

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