

SOT-23 Dual Common Anode Zeners for ESD Protection

These dual monolithic silicon Zener diodes are designed for applications requiring transient overvoltage protection capability. They are intended for use in voltage and ESD sensitive equipment such as computers, printer, business machines, communication systems, medical equipment and other applications. Their dual junction common anode design protects two separate lines using only one package. These devices are ideal for situations where board space is at a premium.

Features

- Pb-free Package are Available
- SOT-23 Package Allows Either Two Separate Unidirectional Configurations or a Single Bidirectional Configuration
- Working Peak Reverse Voltage Range 3V to 26V
- Standard Zener Breakdown Voltage Range 5.6V to 33V
- Peak Power 24 or 40 Watts @ 1.0ms(Unidirectional), per Figure 5 Waveform
- ESD Rating of Class N (exceeding 16KV) per the Human Body Model
- Maximum Clamping Voltage @ Peak Pulse Current
- AEC-Q101 qualified

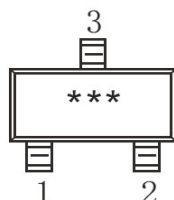
Ordering Information

- Device: MMBZ27VALQ-7-F-CN
- Package: SOT-23
- Material: Halogen free
- Packing: Tape & Reel
- Quantity per reel: 3,000pcs

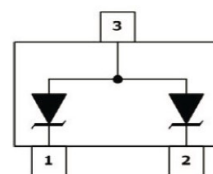
Mechanical Data

- SOT-23 Package
- Flammability Rating: UL 94V-0
- High temperature soldering guaranteed:260°C/10s

Marking code



Pin Configuration



ABSOLUTE MAXIMUM RATING

Parameters	Symbol	Value	Unit
Total Power Dissipation on FR-5 Board (Note 2) @ TA=25°C	P _D	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance Junction-to-Ambient	R _{θJA}	556	°C/W
Total Power Dissipation on Alumina Substrate (Note 3) @ TA=25°C	P _D	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance Junction-to-Ambient	R _{θJA}	417	°C/W
Peak Power Dissipation @ 1.0ms (Note 1) TL≤25°C	P _{PK}	40	W
Junction and Storage temperature range	T _J , T _{STG}	-55+150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1, Non-repetitive current pulse per Figure 5 and derate above TA=25°C per Figure 6;

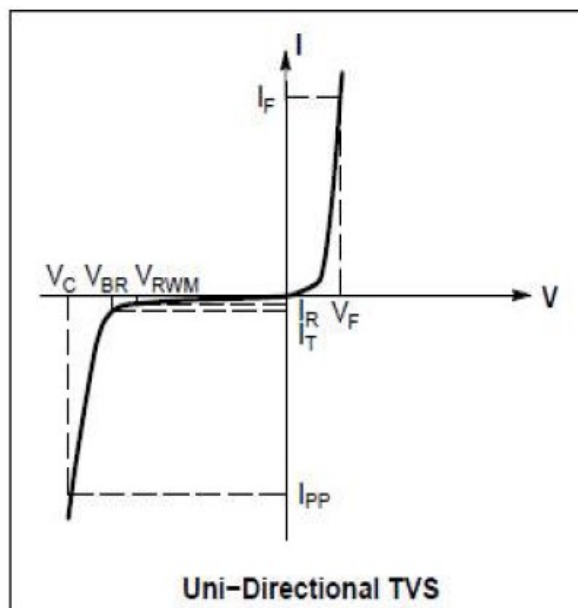
2, FR-5 = 1.0 x 0.75 x 0.62 in;

3, Alumina = 0.4 x 0.3 x 0.024 in, 99.5% alumina

* Other voltages may be available upon request.

ELECTRICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED
UNIDIRECTIONAL (Circuit tied to Pins 1 and 3 or 2 and 3)

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
θV_{BR}	Maximum Temperature Coefficient of V_{BR}
I_F	Forward Current
V_F	Forward Voltage @ I_F
Z_{ZT}	Maximum Zener Impedance @ I_{ZT}
I_{ZK}	Reverse Current
Z_{ZK}	Maximum Zener Impedance @ I_{ZK}


24 WATTS

Device	Device Marking	V _{RWM} Volts	I _R @ V _{RWM} μA	Breakdown Voltage				Max Zener Impedance (Note 5)			V _C @ I _{PP} (Note 6)		θV _{BR} mV/°C
				V _{BR} (Note 4) (V)			@ I _T mA	Z _{ZT} @ I _{ZT} Ω	Z _{ZK} @ I _{ZK} Ω mA	V _C V	I _{PP} A		
				Min	Nom	Max							
MMBZ5V6AL	5A6	3.0	5.0	5.32	5.6	5.88	20	11	1600	0.25	8.0	3.0	1.26
MMBZ6V2AL	6A2	3.0	0.5	5.89	6.2	6.51	1.0	–	–	–	8.7	2.76	2.80
MMBZ6V8AL	6A8	4.5	0.5	6.46	6.8	7.14	1.0	–	–	–	9.6	2.5	3.4
MMBZ9V1AL	9A1	6.0	0.3	8.65	9.1	9.56	1.0	–	–	–	14	1.7	7.5
MMBZ10VAL	10A	6.5	0.3	9.50	10	10.5	1.0	–	–	–	14.2	1.7	7.5

40 WATTS

Device	Device Marking	V _{RWM}	I _R @ V _{RWM}	Breakdown Voltage				V _C @ I _{PP} (Note 6)		θV _{BR} mV/°C
				V _{BR} (Note 4) (V)			@ I _T	V _C	I _{PP}	
		Volts	nA	Min	Nom	Max	mA	V	A	
MMBZ12VAL	12A	8.5	200	11.40	12	12.60	1.0	17	2.35	7.5
MMBZ15VAL	15A	12	50	14.25	15	15.75	1.0	21	1.9	12.3
MMBZ18VAL	18A	14.5	50	17.10	18	18.90	1.0	25	1.6	15.3
MMBZ20VAL	20A	17	50	19.00	20	21.00	1.0	28	1.4	17.2
MMBZ27VAL	27A	22	50	25.65	27	28.35	1.0	41	1.0	24.3
MMBZ33VAL	33A	26	50	31.35	33	34.65	1.0	46	0.87	30.4

4, V_{BR} measured at pulse test current I_T at an ambient temperature of 25°C

5, Z_{ZT} and Z_{ZK} are measured by dividing the AC voltage drop across the device by the AC current applied. The specified limits are for $I_Z(\text{AC})=0.1 I_Z(\text{DC})$, with the AC frequency = 1.0kHz.

6, Surge current waveform per Figure 5 and derate Figure 6

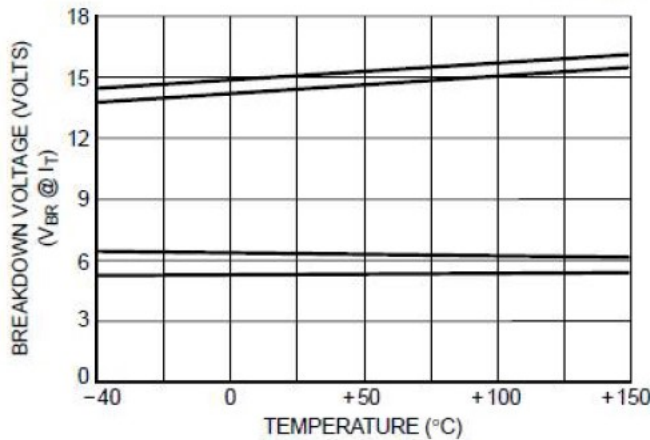
ELECTRICAL CHARACTERISTICS CURVE


Figure 1. Typical Breakdown Voltage versus Temperature

(Upper curve for each voltage is bidirectional mode, lower curve is unidirectional mode)

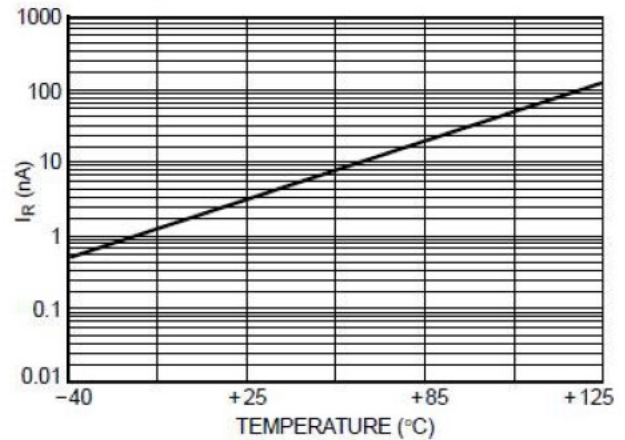


Figure 2. Typical Leakage Current versus Temperature

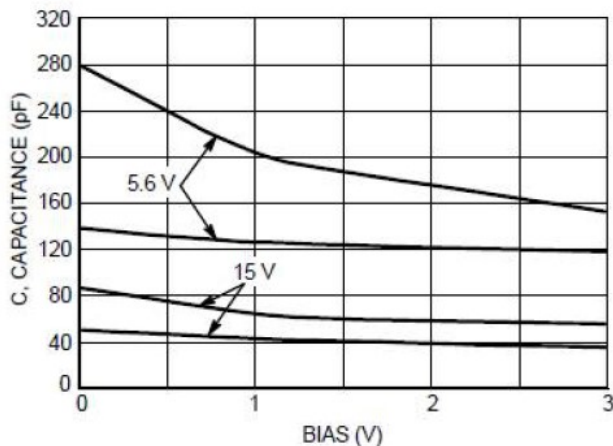


Figure 3. Typical Capacitance versus Bias Voltage

(Upper curve for each voltage is unidirectional mode, lower curve is bidirectional mode)

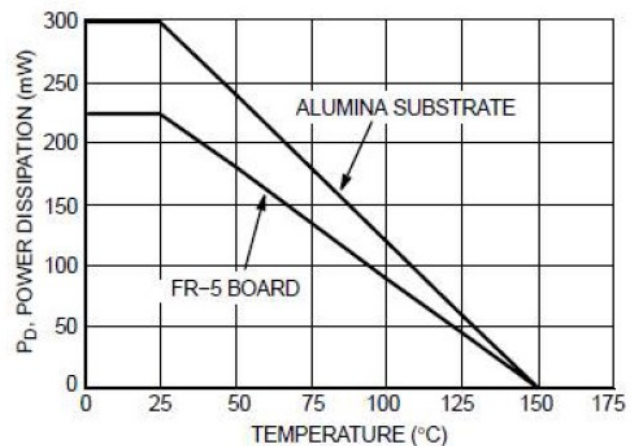


Figure 4. Steady State Power Derating Curve

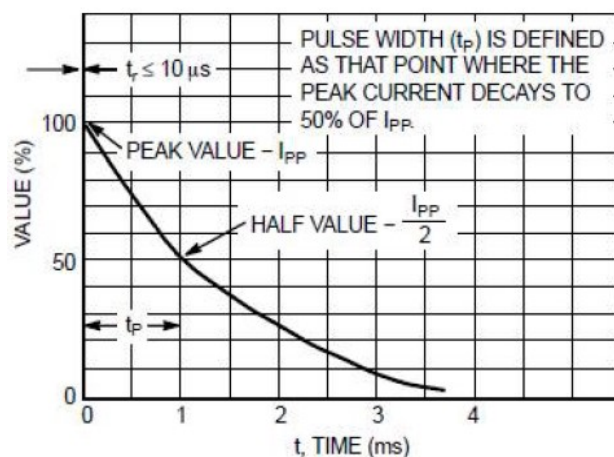


Figure 5. Pulse Waveform

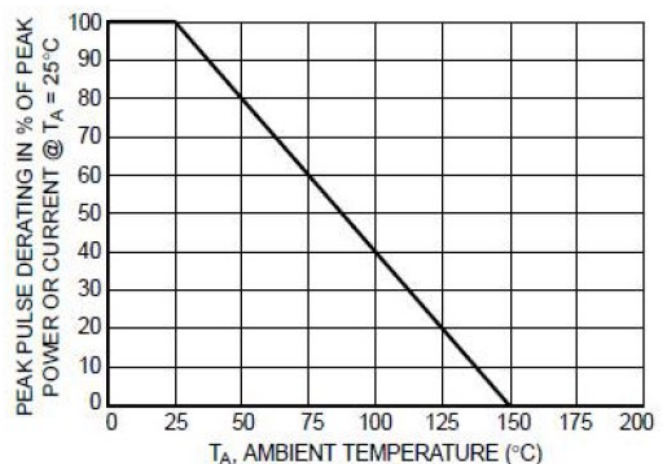


Figure 6. Pulse Derating Curve

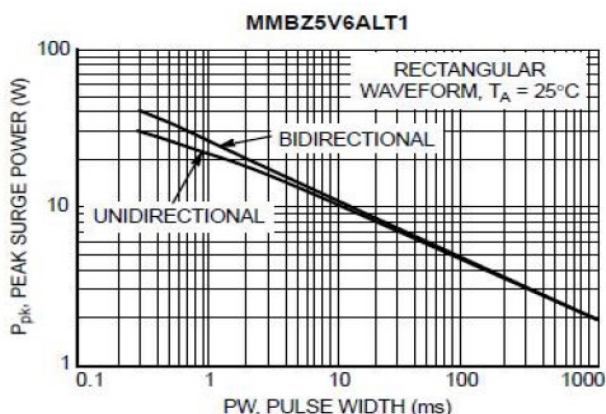


Figure 7. Maximum Non-repetitive Surge Power, P_{pk} versus PW

Power is defined as $V_{RSM} \times I_Z(pk)$ where V_{RSM} is the clamping voltage at $I_Z(pk)$.

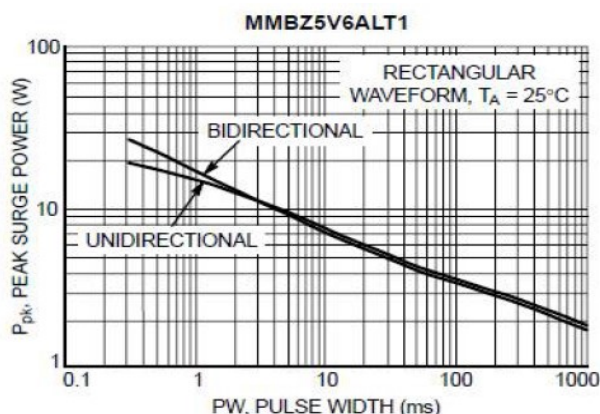


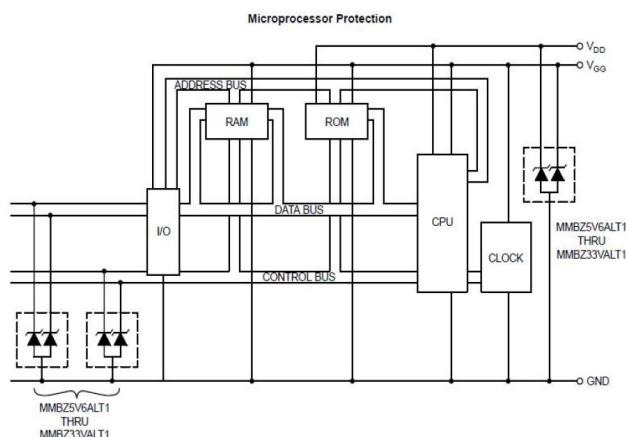
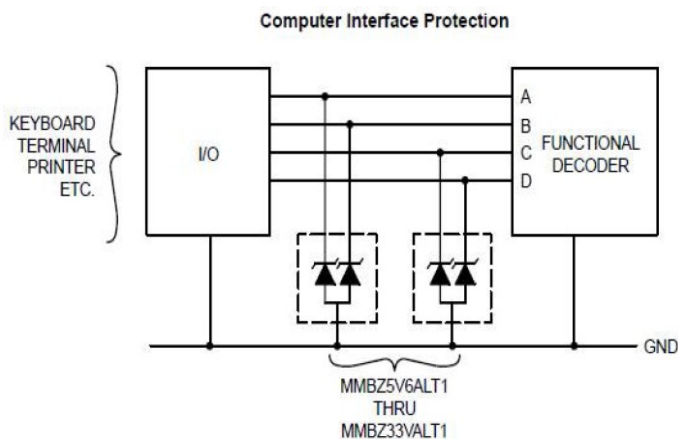
Figure 8. Maximum Non-repetitive Surge Power, $P_{pk}(NOM)$ versus PW

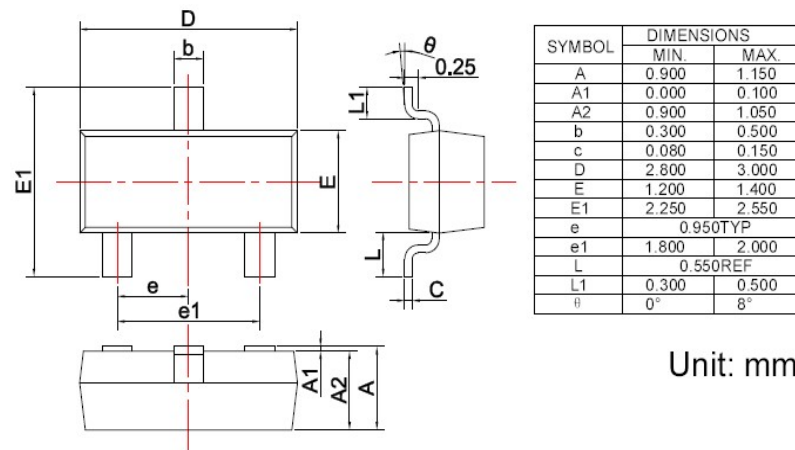
Power is defined as $V_Z(NOM) \times I_Z(pk)$ where $V_Z(NOM)$ is the nominal Zener voltage measured at the low test current used for voltage classification.

TYPICAL COMMON ANODE APPLICATIONS

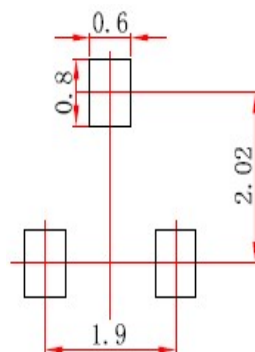
A quad junction common anode design in a SOT-23 package protects four separate lines using only one package. This adds flexibility and creativity to PCB design especially

when board space is at a premium. Two simplified examples of TVS applications are illustrated below.



SOT-23 PACKAGE OUTLINE Plastic surface mounted package


焊盘设计参考 Precautions: PCB Design (Recommended land dimensions for SOT-23 diode. Electrode patterns for PCBs)


Note:

1. Controlling dimension: In millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

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