


SOT-143

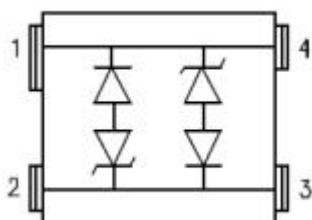
Description

The USB50412CE3/TR7-CN of TVS array have been designed to provide bidirectional protection for sensitive electronics from damage due to voltage transients caused by electrostatic discharge (ESD), electrical fast transients (EFT), secondary lightning and other voltage-induced transient events. The device can be used to protect 1 bidirectional or interface line.

Features

- Protects 3.3,5,12,15,24 V Components
- Bidirectional
- Ultra Low Capacitance 15 pF
- Ultra Low Leakage
- Provides Electrically Isolated Protection
- 500 W @ 8/20 us
- Protects 1 Line
- “-A” is an AEC-Q101 qualified device
- SOT-143 Packaging
- This is a Pb - Free Device
- All SMC parts are traceable to the wafer lot
- Additional testing can be offered upon request

Schematic & Pin Configuration



Mechanical Characteristics

- SOT-143 Surface Mount Package
- Approximate Weight: 0.03 grams
- PIN #1 Indicator: DOT on top of package
- Packaging: Tape and Reel Per EIA 481

Application

- LAN/WAN Equipment
- Cellular Phone
- Notebooks, Desktops, & Servers
- Audio/Video Inputs
- Handheld Electronics
- Fire Wire, SCSI & USB interfaces

Maximum Ratings@T_A=25°C unless otherwise specified

Parameter	Symbol	Value	Units
Peak Pulse Power, 8/20 μs Wave shape	P	500	W
Operating Temperature	T _J	-55 to +125	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Lead Soldering Temperature	T _L	260 (10 Sec.)	°C

Electrical Characteristics@T_A=25°C unless otherwise specified

Part Number	Device Code	Stand-off Voltage V _{WM} (V) Max	Breakdown Voltage V _{BR} @1mA (V) Min	Clamping Voltage V _C @ 1 A (V) Max	Leakage Current I _R @ V _{WM} (uA) Max	Capacitance (f = 1MHz) C @ 0V (pF) Max	Temperature Coefficient of V _{BR} a(V _{BR}) mv/°C Max
USB50412CE3/ TR7-CN	L12	12.0	13.3	19	1	15	8

Ratings and Characteristics Curves

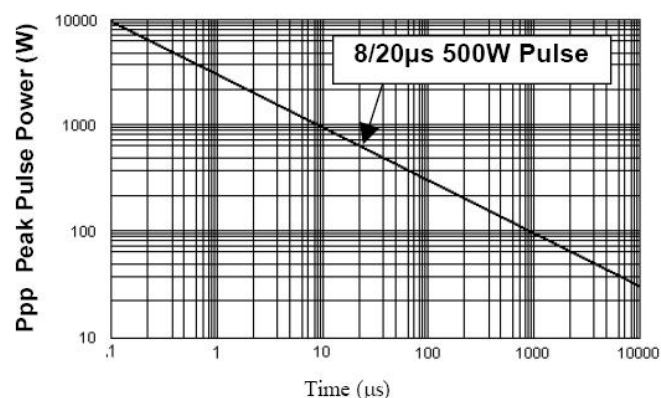


Figure 1. Peak Pulse Power Vs Pulse Time (μs)

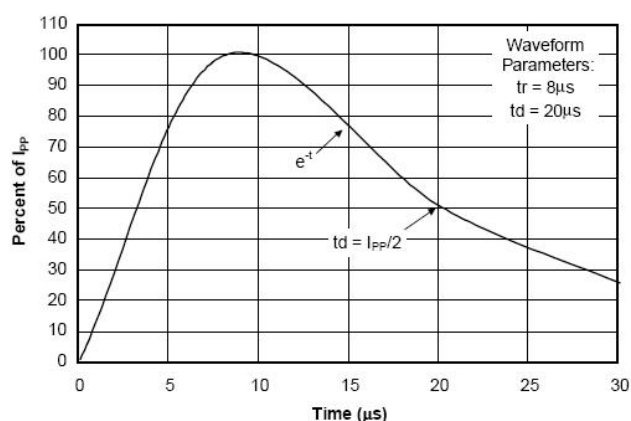


Figure 2. Pulse Wave Form

Ordering Information

Device	Package	Shipping
USB50412CE3/TR7-CN	SOT-143 (Pb-Free)	3000pcs / reel

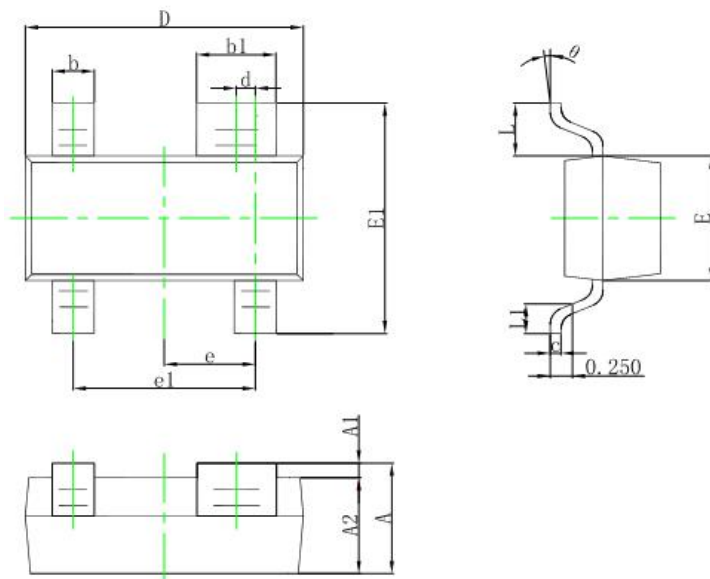
For information on tape and reel specifications, including part orientation and tape sizes, please refer to our tape and reel packaging specification.

Marking Diagram



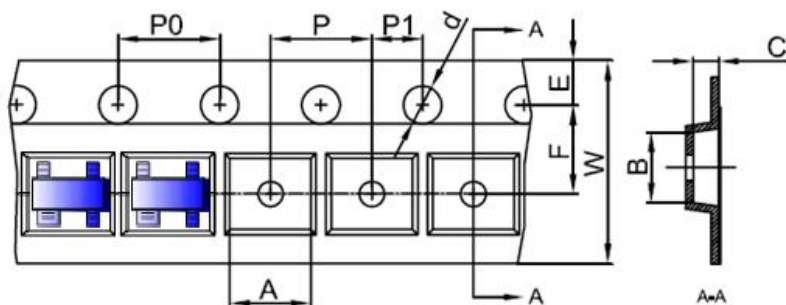
L05 = Marking Code

Mechanical Dimensions SOT-143



SYMBOL	Millimeters		Inches	
	MIN.	MAX.	MIN.	MAX.
A	0.890	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.076	0.170	0.003	0.007
D	2.650	3.050	0.104	0.120
E	1.190	1.400	0.047	0.055
E1	2.100	2.550	0.083	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.780	2.050	0.070	0.081
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

Carrier Tape Specification SOT-143



SYMBOL	Millimeters	
	Min.	Max.
A	3.09	3.29
B	2.70	2.90
C	1.21	1.41
d	1.40	1.60
E	1.65	1.85
F	3.40	3.60
P	3.90	4.10
P0	3.90	4.10
P1	1.90	2.10
W	7.90	8.10

NOTICE

The information presented in this document is for reference only. Involving product optimization and productivity improvement, ChipNobo reserves the right to adjust product indicators and upgrade some technical parameters. ChipNobo is entitled to be exempted from liability for any delay or non-delivery of the information disclosure process that occurs.

本文件中提供的信息仅供参考。涉及产品优化和生产效率改善，ChipNobo 有权调整产品指标和部分技术参数的升级，所出现信息披露过程存在延后或者不能送达的情形，ChipNobo 有获免责权。

The product listed herein is designed to be used with residential and commercial equipment, and do not support sensitive items and specialized equipment in areas where sanctions do exist. ChipNobo Co., Ltd or anyone on its behalf, assumes no responsibility or liability for any damages resulting from improper use.

此处列出的产品旨在民用和商业设备上使用，不支持确有制裁地区的敏感项目和特殊设备，ChipNobo 有限公司或其代表，对因不当使用而造成的任何损害不承担任何责任。

For additional information, please visit our website <http://www.chipnobo.com>, or consult your nearest Chipnobo sales office for further assistance.

欲了解更多信息，请访问我们的网站 <http://www.chipnobo.com>，或咨询离您最近的 Chipnobo 销售办事处以获得进一步帮助。