

Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Parameter	Value	Units
Power Dissipation	500	mW
Storage Temperature Range	-65 to +175	$^\circ\text{C}$
Operating Junction Temperature	+175	$^\circ\text{C}$

These ratings are limiting values above which the serviceability of the diode may be impaired.



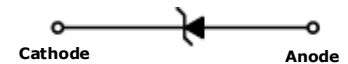
**SURFACE MOUNT
LL34**

DEVICE MARKING DIAGRAM


Cathode Band Color : Blue

Specification Features:

Zener Voltage Range 2.0 to 75 Volts
 LL-34 (Mini-MELF) Package
 Surface Device Type Mounting
 Hermetically Sealed Glass
 Compression Bonded Construction
 All External Surfaces Are Corrosion Resistant And Terminals Are Readily Solderable
 RoHS Compliant
 Matte Tin (Sn) Terminal Finish
 Color band Indicates Negative Polarity



ELECTRICAL SYMBOL

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Device Type	$V_Z @ I_{ZT}$ (Volts) Nominal	I_{ZT} (mA)	$Z_{ZT} @ I_{ZT}$ (Ω) Max	$I_R @ V_R$ (μA) Max	V_R (Volts)
BZV55C43	43	2	90	0.2	33

V_F Forward Voltage = 1.2 V Maximum @ $I_F = 200$ mA for all types

Notes:

1. The type number listed have zener voltage min/max limits as shown and have a standard tolerance on the nominal zener voltage of 5%.
2. For detailed information on price, availability and delivery of nominal zener voltages between the voltages shown and tighter voltage tolerances, contact your nearest ChipNobo Electronics representative.
3. The zener impedance is derived from the 60-cycle ac voltage, which results when an ac current having an rms value equal to 10% of the dc zener current (I_{ZT} or I_{ZK}) is superimposed to I_{ZT} or I_{ZK} .

Typical Characteristics

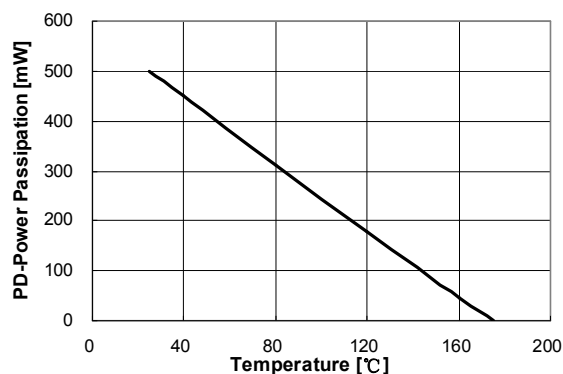


Figure 1. Power Dissipation vs Ambient Temperature
Valid provided leads at a distance of 0.8mm from case are kept at ambient temperature

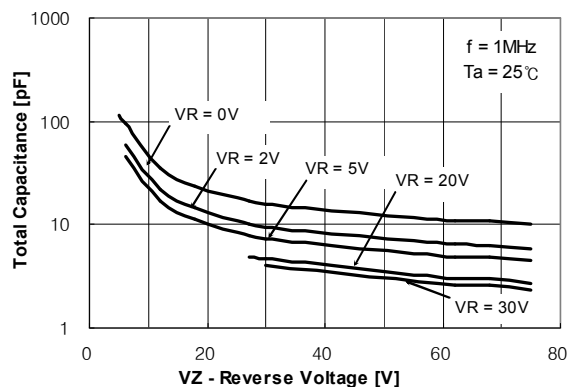


Figure 2. Total Capacitance

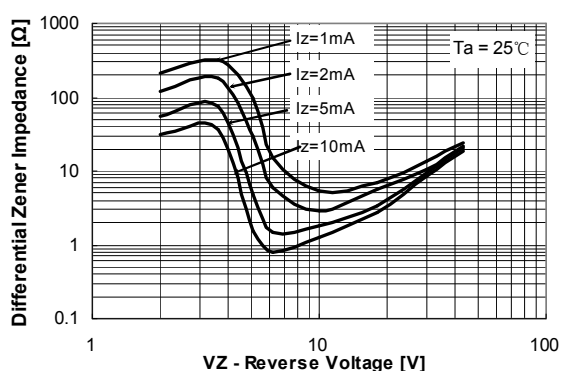


Figure 3. Differential Impedance vs. Zener Voltage

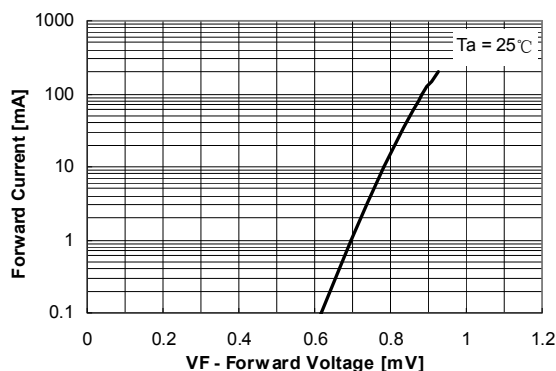


Figure 4. Forward Current vs. Forward Voltage

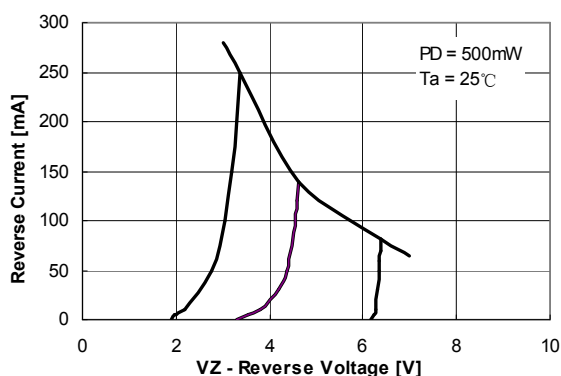


Figure 5. Reverse Current vs. Reverse Voltage

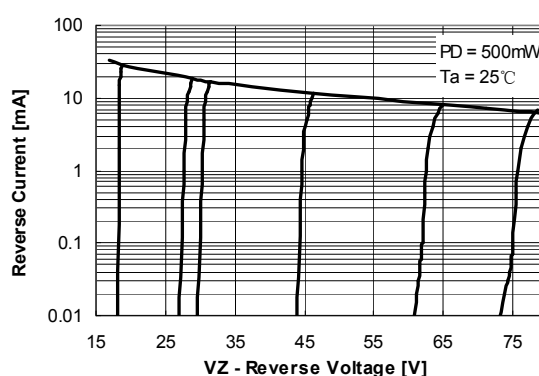
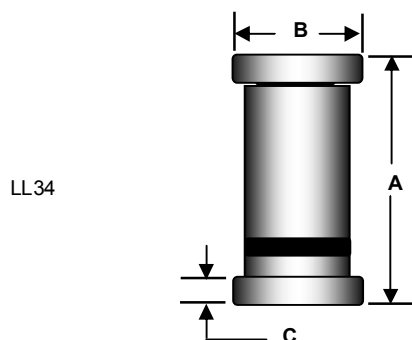


Figure 6. Reverse Current vs. Reverse Voltage

Package Outline

Package



Case Outline

DIM	LL-34			
	Millimeters		Inches	
	Min	Max	Min	Max
A	3.30	3.60	0.130	0.142
B	1.40	1.50	0.055	0.059
C	0.35	0.50	0.014	0.020

Notes:

1. All dimensions are within DO213AC JEDEC standard.
2. LL-34 polarity denoted by cathode band.

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