

Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Parameter	Value	Units
Power Dissipation	500	mW
Storage Temperature Range	-65 to +175	$^\circ\text{C}$
Operating Junction Temperature	+175	$^\circ\text{C}$

These ratings are limiting values above which the serviceability of the diode may be impaired.



**SURFACE MOUNT
LL34**

Specification Features:

Zener Voltage Range 2.4 to 75 Volts

VZ tolerance $\pm 5\%$

LL-34 (Mini-MELF) Package

Surface Device Type Mounting

Hermetically Sealed Glass

Compression Bonded Construction

All External Surfaces Are Corrosion Resistant And Terminals Are Readily Solderable

RoHS Compliant

Matte Tin (Sn) Terminal Finish

Color band Indicates Negative Polarity

DEVICE MARKING DIAGRAM


Cathode Band Color : Blue


ELECTRICAL SYMBOL
Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Device Type	$V_Z @ I_{ZT}$ (Volts)			I_{ZT} (mA)	$Z_{ZT} @ I_{ZT}$ (Ω) Max	I_{ZK} (mA)	$Z_{ZK} @ I_{ZK}$ (Ω) Max	$I_R @ V_R$ (μA) Max	V_R (Volts)
	Min	Nom	Max						
BZV55B2V7	2.57	2.7	2.84	5	94	1	564	18	1

V_F Forward Voltage = 1 V Maximum @ $I_F = 100$ mA for all types

Notes:

- For detailed information on price, availability and delivery of nominal zener voltages between the voltages shown and tighter voltage tolerances, contact your nearest ChipNobo Electronics representative.
- The zener impedance is derived from the 60-cycle ac voltage, which results when an ac current having an rms value equal to 10% of the dc zener current (I_{ZT} or I_{ZK}) is superimposed to I_{ZT} or I_{ZK} .

Typical Characteristics

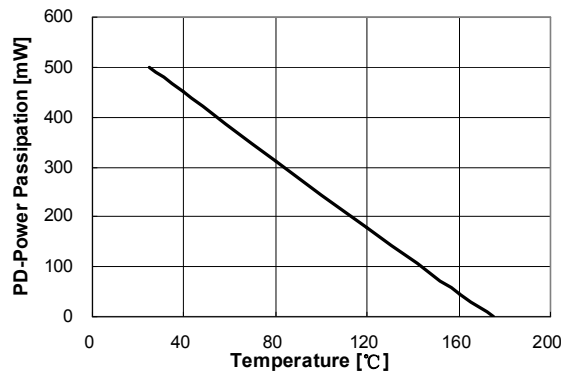


Figure 1. Power Dissipation vs Ambient Temperature
Valid provided leads at a distance of 0.8mm from case are kept at ambient temperature

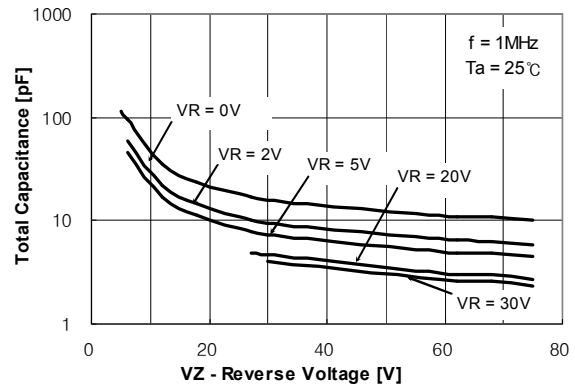


Figure 2. Total Capacitance

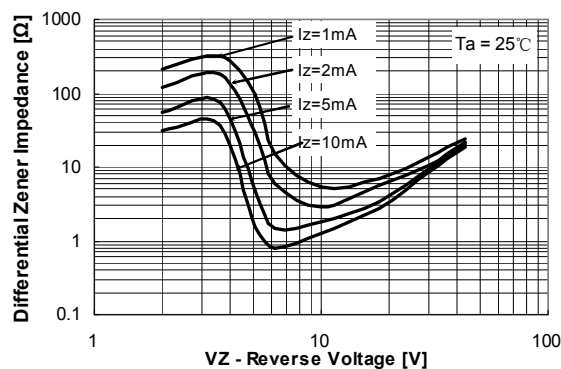


Figure 3. Differential Impedance vs. Zener Voltage

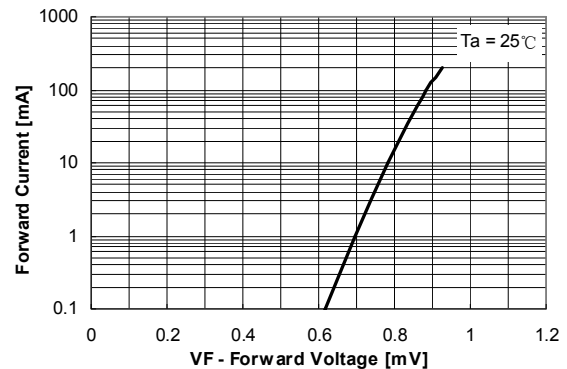


Figure 4. Forward Current vs. Forward Voltage

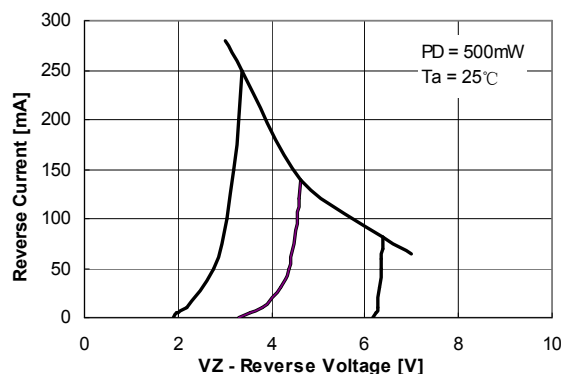


Figure 5. Reverse Current vs. Reverse Voltage

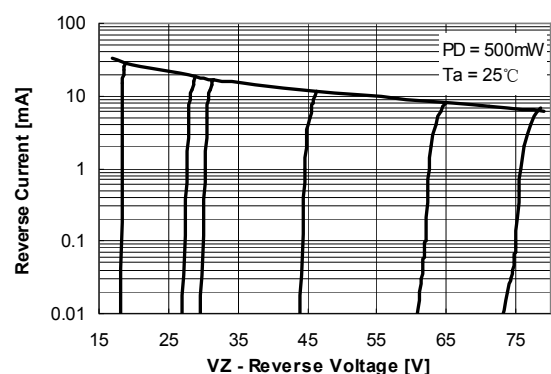
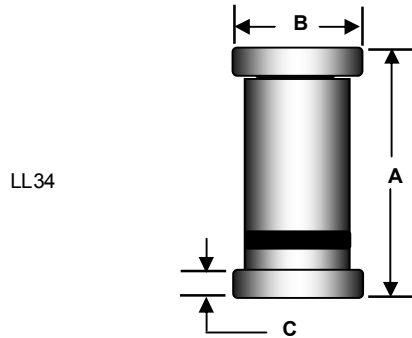


Figure 6. Reverse Current vs. Reverse Voltage

Package Outline

Package



Case Outline

DIM	LL-34			
	Millimeters		Inches	
	Min	Max	Min	Max
A	3.30	3.60	0.130	0.142
B	1.40	1.50	0.055	0.059
C	0.35	0.50	0.014	0.020

Notes:

1. All dimensions are within DO213AC JEDEC standard.
2. LL-34 polarity denoted by cathode band.

NOTICE

The information presented in this document is for reference only. Involving product optimization and productivity improvement, ChipNobo reserves the right to adjust product indicators and upgrade some technical parameters. ChipNobo is entitled to be exempted from liability for any delay or non-delivery of the information disclosure process that occurs.

本文件中提供的信息仅供参考。涉及产品优化和生产效率改善，ChipNobo 有权调整产品指标和部分技术参数的升级，所出现信息披露过程存在延后或者不能送达的情形，ChipNobo 有获免责权。

The product listed herein is designed to be used with residential and commercial equipment, and do not support sensitive items and specialized equipment in areas where sanctions do exist. ChipNobo Co., Ltd or anyone on its behalf, assumes no responsibility or liability for any damages resulting from improper use.

此处列出的产品旨在民用和商业设备上使用，不支持确有制裁地区的敏感项目和特殊设备，ChipNobo 有限公司或其代表，对因不当使用而造成的任何损害不承担任何责任。

For additional information, please visit our website <https://www.chipnobo.com/en> or consult your nearest Chipnobo sales office for further assistance.

欲了解更多信息，请访问我们的网站 <https://www.chipnobo.com/en>，或咨询离您最近的 Chipnobo 销售办事处以获得进一步帮助。